

74AHC594-Q100; 74AHCT594-Q100

8-bit shift register with output register

Rev. 2 — 4 July 2013

Product data sheet

1. General description

The 74AHC594-Q100; 74AHCT594-Q100 is a high-speed Si-gate CMOS device and is pin compatible with Low-Power Schottky TTL (LSTTL). It is specified in compliance with JEDEC standard No. 7A.

The 74AHC594-Q100; 74AHCT594-Q100 is an 8-bit, non-inverting, serial-in, parallel-out shift register that feeds an 8-bit D-type storage register. Separate clocks (SHCP and STCP) and direct overriding clears (SHR and STR) are provided on both the shift and storage registers. A serial output (Q7S) is provided for cascading purposes.

Both the shift and storage register clocks are positive-edge triggered. If the user wishes to connect both clocks together, the shift register is always one count pulse ahead of the storage register.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - ◆ Specified from $-40\text{ }^{\circ}\text{C}$ to $+85\text{ }^{\circ}\text{C}$ and from $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$
- Balanced propagation delays
- All inputs have Schmitt trigger actions
- Inputs accept voltages higher than V_{CC}
- Wide supply voltage range from 2.0 V to 5.5 V
- 8-bit serial-in, parallel-out shift register with storage
- Independent direct overriding clears on shift and storage registers
- Independent clocks for shift and storage registers
- Latch-up performance exceeds 100 mA per JESD78 Class II
- Input levels:
 - ◆ For 74AHC594-Q100: CMOS level
 - ◆ For 74AHCT594-Q100: TTL level
- ESD protection:
 - ◆ MIL-STD-883, method 3015 exceeds 2000 V
 - ◆ HBM JESD22-A114F exceeds 2000 V
 - ◆ MM JESD22-A115-A exceeds 200 V ($C = 200\text{ pf}$, $R = 0\text{ }\Omega$)
- Multiple package options

3. Applications

- Serial-to parallel data conversion
- Remote control holding register

4. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AHC594-Q100				
74AHC594D-Q100	–40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1
74AHC594DB-Q100	–40 °C to +125 °C	SSOP16	plastic shrink small outline package; 16 leads; body width 5.3 mm	SOT338-1
74AHC594PW-Q100	–40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	SOT403-1
74AHC594BQ-Q100	–40 °C to +125 °C	DHVQFN16	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 16 terminals; body 2.5 × 3.5 × 0.85 mm	SOT763-1
74AHCT594-Q100				
74AHCT594D-Q100	–40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1
74AHCT594DB-Q100	–40 °C to +125 °C	SSOP16	plastic shrink small outline package; 16 leads; body width 5.3 mm	SOT338-1
74AHCT594PW-Q100	–40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	SOT403-1
74AHCT594BQ-Q100	–40 °C to +125 °C	DHVQFN16	plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 16 terminals; body 2.5 × 3.5 × 0.85 mm	SOT763-1

5. Functional diagram

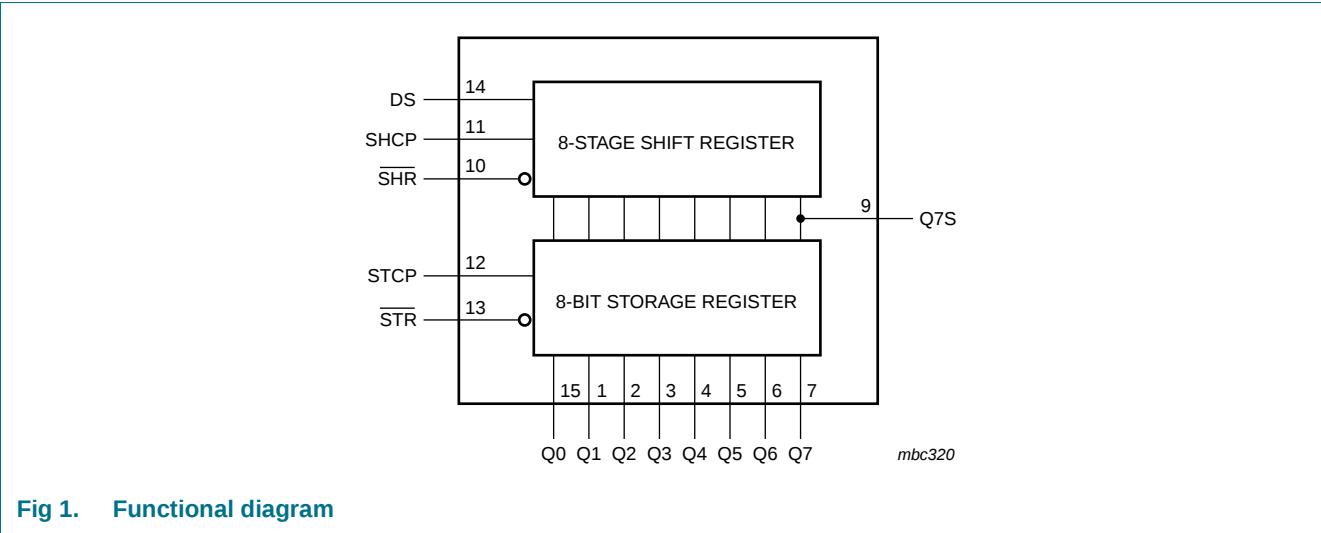


Fig 1. Functional diagram

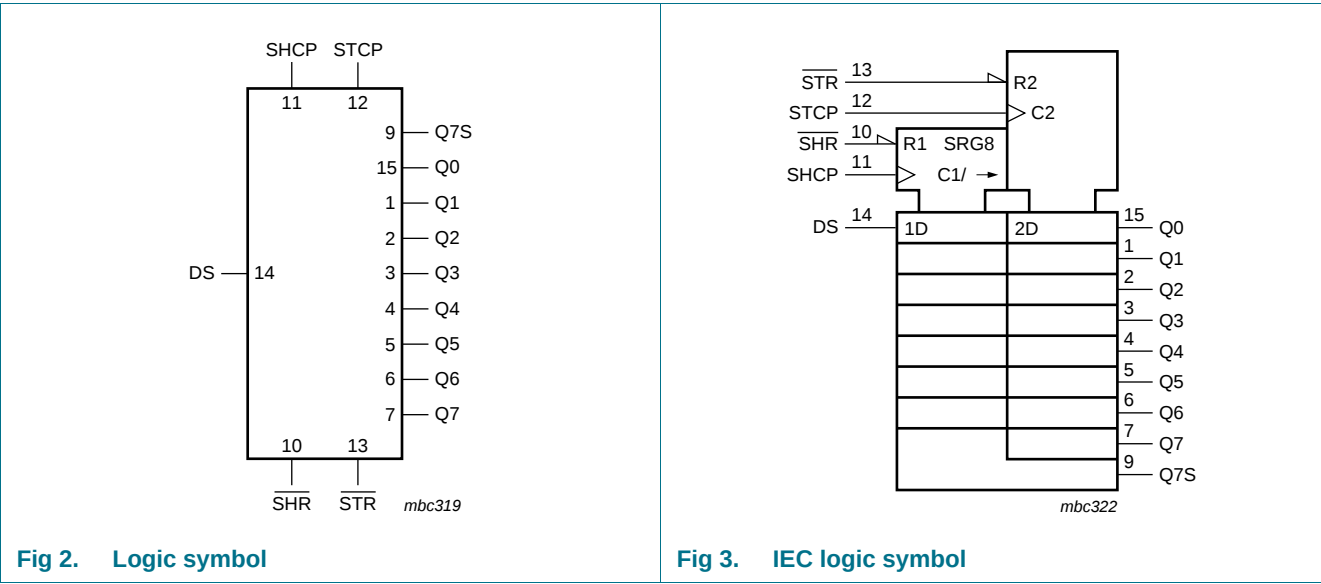


Fig 2. Logic symbol

Fig 3. IEC logic symbol

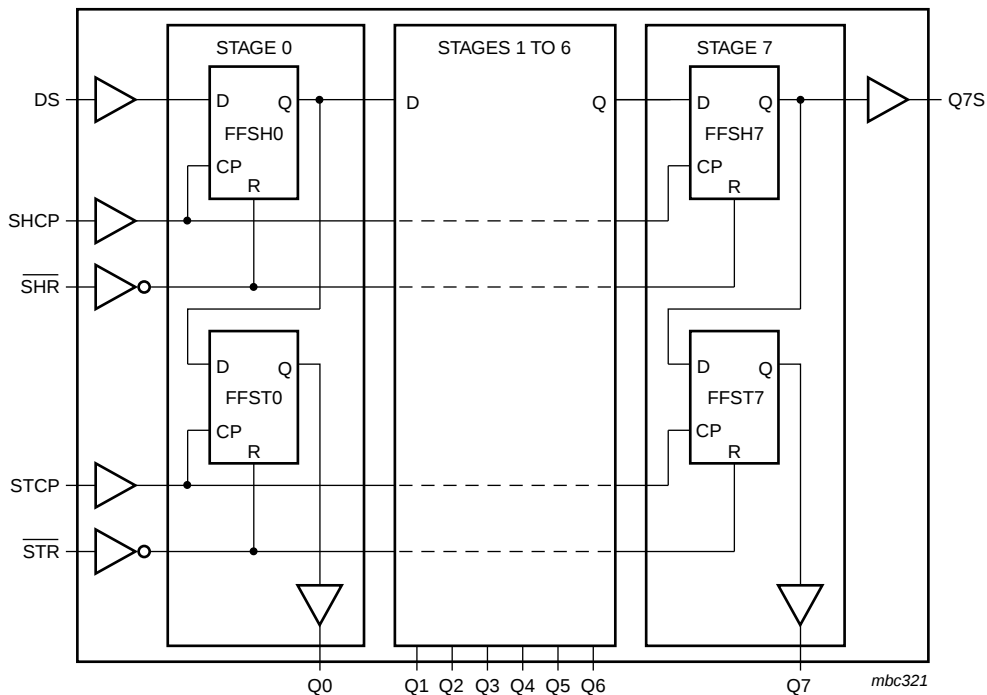


Fig 4. Logic diagram

6. Pinning information

6.1 Pinning

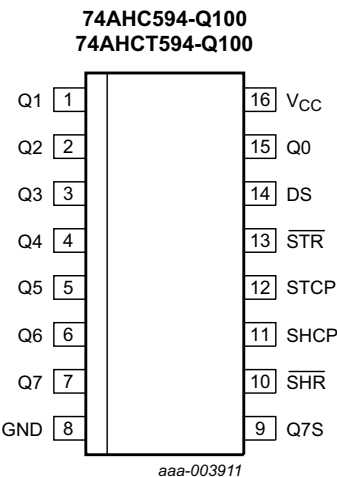
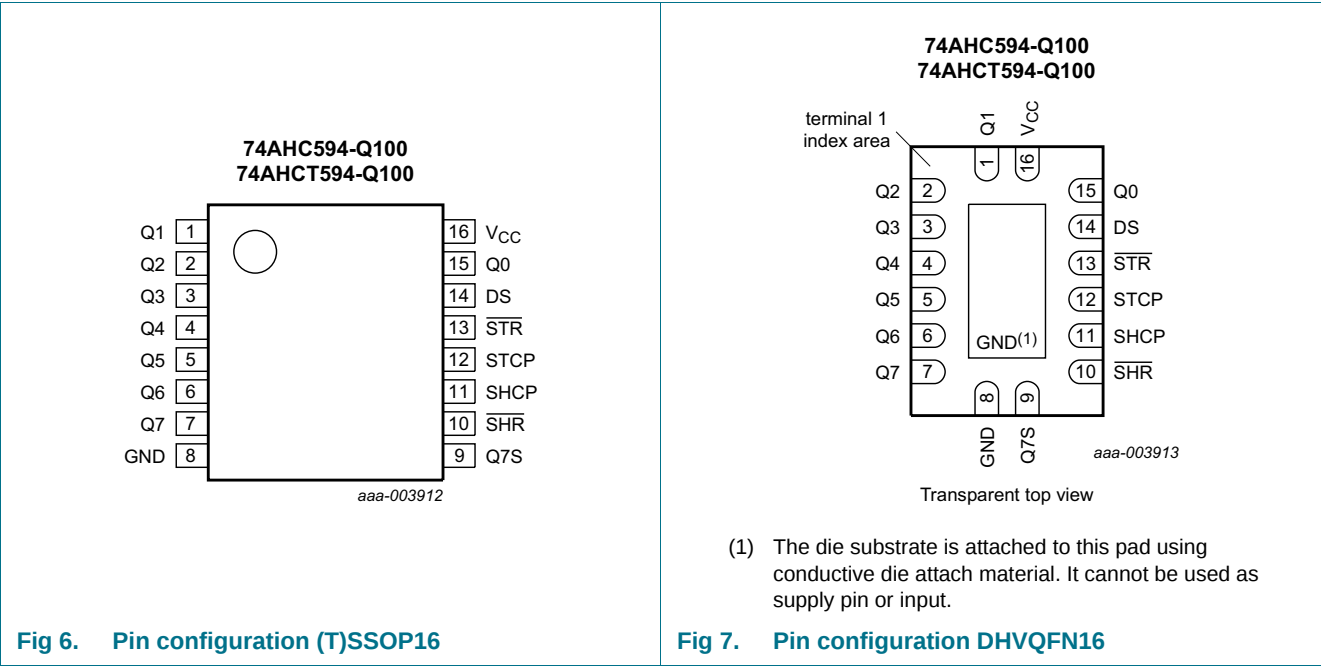


Fig 5. Pin configuration SO16



6.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
Q1	1	parallel data output
Q2	2	parallel data output
Q3	3	parallel data output
Q4	4	parallel data output
Q5	5	parallel data output
Q6	6	parallel data output
Q7	7	parallel data output
GND	8	ground (0 V)
Q7S	9	serial data output
SHR	10	shift register reset input (active LOW)
SHCP	11	shift register clock input
STCP	12	storage register clock input
STR	13	storage register reset input (active LOW)
DS	14	serial data input
Q0	15	parallel data output
V _{CC}	16	supply voltage

7. Functional description

Table 3. Function table^[1]

Input					Output		Function
SHCP	STCP	SHR	STR	DS	Q7S	Qn	
X	X	L	X	X	L	NC	a LOW-state on $\overline{\text{SHR}}$ only affects the shift register
X	X	X	L	X	NC	L	a LOW-state on $\overline{\text{STR}}$ only affects the storage register
X	↑	L	H	X	L	L	empty shift register loaded into storage register
↑	X	H	X	H	Q6S	NC	logic HIGH level shifted into shift register stage 0. Contents of all shift register stages shifted through, e.g. previous state of stage 6 (internal Q6S) appears on the serial output (Q7S).
X	↑	H	H	X	NC	QnS	contents of shift register stages (internal QnS) are transferred to the storage register and parallel output stages
↑	↑	H	H	X	Q6S	QnS	contents of shift register shifted through; previous contents of the shift register is transferred to the storage register and the parallel output stages

[1] H = HIGH voltage state;
L = LOW voltage state;
↑ = LOW to HIGH transition;
X = don't care;
NC = no change;

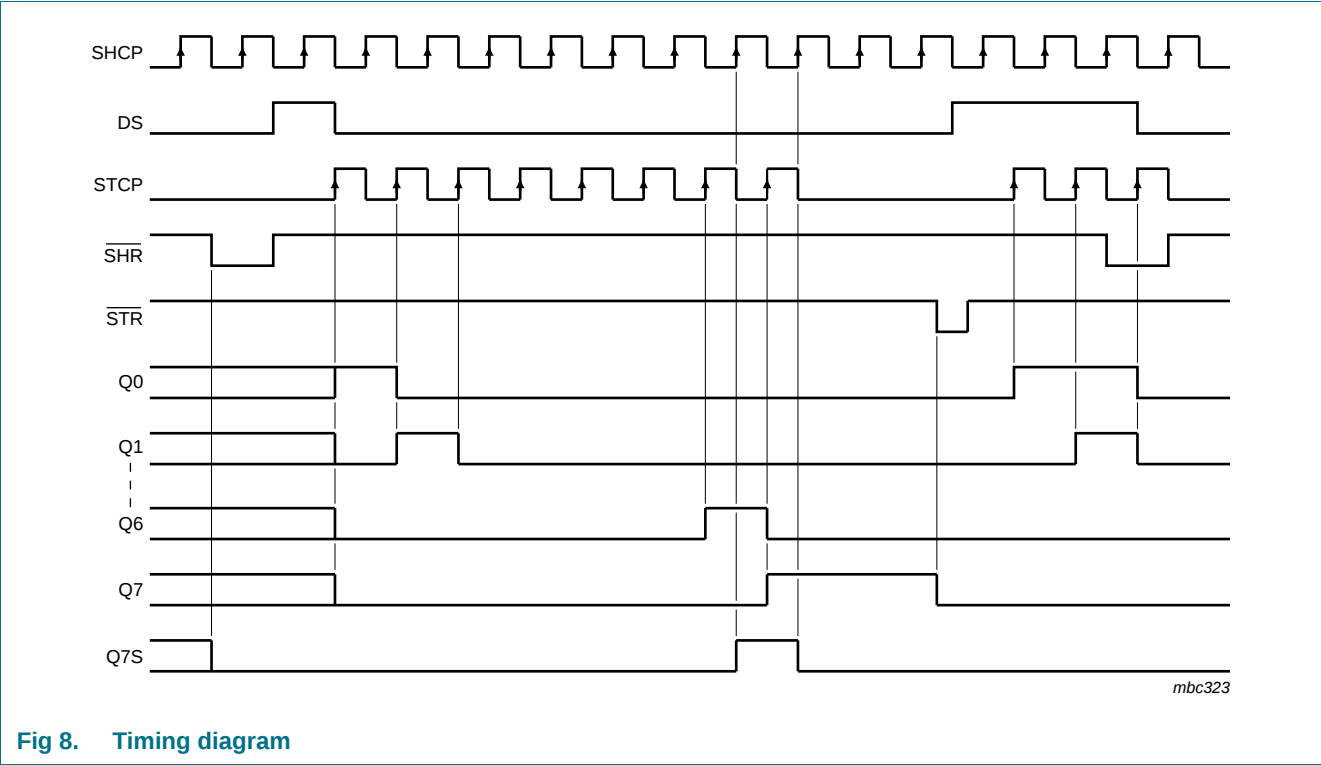


Fig 8. Timing diagram

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7.0	V
V_I	input voltage		-0.5	+7.0	V
I_{IK}	input clamping current	$V_I < -0.5$ V	[1] -20	-	mA
I_{OK}	output clamping current	$V_O < -0.5$ V or $V_O > V_{CC} + 0.5$ V	[1] -20	+20	mA
I_O	output current	$V_O = -0.5$ V to $(V_{CC} + 0.5)$ V	-25	+25	mA
I_{CC}	supply current		-	+75	mA
I_{GND}	ground current		-75	-	mA
T_{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40$ °C to +125 °C	[2] -	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SO16 packages: above 70 °C the value of P_{tot} derates linearly at 8 mW/K.

For (T)SSOP16 packages: above 60 °C the value of P_{tot} derates linearly at 5.5 mW/K.

For DHVQFN16 packages: above 60 °C the value of P_{tot} derates linearly at 4.5 mW/K.

9. Recommended operating conditions

Table 5. Operating conditions

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
74AHC594-Q100						
V_{CC}	supply voltage		2.0	5.0	5.5	V
V_I	input voltage		0	-	5.5	V
V_O	output voltage		0	-	V_{CC}	V
T_{amb}	ambient temperature		-40	+25	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 3.0\text{ V to }3.6\text{ V}$	-	-	100	ns/V
		$V_{CC} = 4.5\text{ V to }5.5\text{ V}$	-	-	20	ns/V
74AHCT594-Q100						
V_{CC}	supply voltage		4.5	5.0	5.5	V
V_I	input voltage		0	-	5.5	V
V_O	output voltage		0	-	V_{CC}	V
T_{amb}	ambient temperature		-40	+25	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 4.5\text{ V to }5.5\text{ V}$	-	-	20	ns/V

10. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			−40 °C to +85 °C		−40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
74AHC594-Q100										
V _{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
		V _{CC} = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V _{CC} = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
		V _{CC} = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V _{CC} = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}								
		I _O = −50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = −50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = −50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = −4.0 mA; V _{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I _O = −8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.80	-	3.70	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}								
		I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V

Table 6. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
I_I	input leakage current	$V_I = 5.5 \text{ V}$ or GND; $V_{CC} = 0 \text{ V}$ to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I_{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0 \text{ A}$; $V_{CC} = 5.5 \text{ V}$	-	-	4.0	-	40	-	80	μA
C_I	input capacitance	$V_I = V_{CC}$ or GND	-	3	10	-	10	-	10	pF
74AHCT594-Q100										
V_{IH}	HIGH-level input voltage	$V_{CC} = 4.5 \text{ V}$ to 5.5 V	2.0	-	-	2.0	-	2.0	-	V
V_{IL}	LOW-level input voltage	$V_{CC} = 4.5 \text{ V}$ to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}								
		$I_O = -50 \mu\text{A}$; $V_{CC} = 4.5 \text{ V}$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_O = -8.0 \text{ mA}$; $V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.80	-	3.70	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}								
		$I_O = 50 \mu\text{A}$; $V_{CC} = 4.5 \text{ V}$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 8 \text{ mA}$; $V_{CC} = 4.5 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
I_I	input leakage current	$V_I = 5.5 \text{ V}$ or GND; $V_{CC} = 0 \text{ V}$ to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I_{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0 \text{ A}$; $V_{CC} = 5.5 \text{ V}$	-	-	4.0	-	40	-	80	μA
ΔI_{CC}	additional supply current	per input pin; $V_I = V_{CC} - 2.1 \text{ V}$; other pins at V_{CC} or GND; $I_O = 0 \text{ A}$; $V_{CC} = 4.5 \text{ V}$ to 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
C_I	input capacitance	$V_I = V_{CC}$ or GND	-	3	10	-	10	-	10	pF

11. Dynamic characteristics

Table 7. Dynamic characteristicsVoltages are referenced to GND (ground = 0 V); for test circuit see [Figure 15](#).

Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	Min	Max	
74AHC594-Q100										
t _{PLH}	LOW to HIGH propagation delay	SHCP to Q7S; see Figure 9								
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.2	8.5	2.2	9.7	2.2	10.6	ns
		C _L = 50 pF	-	7.4	11.5	3.0	13.2	3.0	14.3	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.8	6.3	1.7	7.2	1.7	7.8	ns
		C _L = 50 pF	-	4.8	8.0	2.4	9.1	2.4	10.0	ns

Table 7. Dynamic characteristics ...continuedVoltages are referenced to GND (ground = 0 V); for test circuit see [Figure 15](#).

Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	Min	Max	
t _{PHL}	HIGH to LOW propagation delay	STCP to Q _n ; see Figure 10								
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.1	8.3	2.3	9.5	2.3	10.6	ns
		C _L = 50 pF	-	7.3	11.9	3.3	13.6	3.3	14.7	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.5	5.7	1.8	6.5	1.8	7.1	ns
		C _L = 50 pF	-	4.8	7.8	2.6	9.0	2.6	9.8	ns
		SHCP to Q7S; see Figure 9								
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.5	8.9	2.3	10.2	2.3	11.0	ns
		C _L = 50 pF	-	7.4	12.1	3.0	13.9	3.0	15.1	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	4.1	6.7	1.9	7.6	1.9	8.2	ns
		C _L = 50 pF	-	5.4	8.8	2.5	10.1	2.5	11.0	ns
		STCP to Q _n ; see Figure 10								
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.5	9.1	2.4	10.4	2.4	11.3	ns
		C _L = 50 pF	-	7.3	12.0	3.2	13.8	3.2	15.0	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.7	6.0	1.9	6.9	1.9	7.5	ns
		C _L = 50 pF	-	5.2	8.5	2.6	9.7	2.6	10.5	ns
		SHR to Q7S; see Figure 13								
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.7	9.5	2.3	10.8	2.3	11.7	ns
		C _L = 50 pF	-	7.5	12.2	3.6	14.0	3.6	15.2	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	4.1	6.7	2.0	7.6	2.0	8.2	ns
		C _L = 50 pF	-	5.4	8.8	2.8	10.1	2.8	11.0	ns
		STR to Q _n ; see Figure 14								
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.8	9.6	2.8	11.0	2.8	12.0	ns
		C _L = 50 pF	-	7.7	12.5	3.8	14.4	3.8	15.6	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	4.1	7.2	2.2	8.2	2.2	8.9	ns
		C _L = 50 pF	-	5.4	9.4	3.0	10.7	3.0	11.6	ns
f _{max}	maximum frequency	SHCP or STCP; see Figure 9 and Figure 10								
		V _{CC} = 3.0 V to 3.6 V	80	125	-	70	-	65	-	MHz
		V _{CC} = 4.5 V to 5.5 V	90	170	-	80	-	70	-	MHz

Table 7. Dynamic characteristics ...continuedVoltages are referenced to GND (ground = 0 V); for test circuit see [Figure 15](#).

Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	Min	Max	
t _w	pulse width	SHCP and STCP HIGH or LOW; see Figure 9 and Figure 10								
		V _{CC} = 3.0 V to 3.6 V	6.0	-	-	6.5	-	7.0	-	ns
		V _{CC} = 4.5 V to 5.5 V	5.5	-	-	6.0	-	6.5	-	ns
		SHR and STR HIGH or LOW; see Figure 13 and Figure 14								
		V _{CC} = 3.0 V to 3.6 V	5.0	-	-	5.0	-	5.5	-	ns
		V _{CC} = 4.5 V to 5.5 V	5.0	-	-	5.2	-	5.7	-	ns
t _{su}	set-up time	DS to SHCP; see Figure 11								
		V _{CC} = 3.0 V to 3.6 V	3.5	-	-	3.5	-	4.0	-	ns
		V _{CC} = 4.5 V to 5.5 V	3.0	-	-	3.0	-	3.5	-	ns
		SHR to STCP; see Figure 12								
		V _{CC} = 3.0 V to 3.6 V	8.0	-	-	9.0	-	9.5	-	ns
		V _{CC} = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.5	-	ns
		SHCP to STCP; see Figure 10								
		V _{CC} = 3.0 V to 3.6 V	8.0	-	-	8.5	-	9.0	-	ns
V _{CC} = 4.5 V to 5.5 V	5.0	-	-	5.0	-	5.5	-	ns		
t _h	hold time	DS to SHCP; see Figure 11								
		V _{CC} = 3.0 V to 3.6 V	1.5	-	-	1.5	-	2.0	-	ns
		V _{CC} = 4.5 V to 5.5 V	2.0	-	-	2.0	-	2.5	-	ns
t _{rec}	recovery time	SHR to SHCP; see Figure 13								
		V _{CC} = 3.0 V to 3.6 V	4.2	-	-	4.8	-	5.3	-	ns
		V _{CC} = 4.5 V to 5.5 V	2.9	-	-	3.3	-	3.8	-	ns
		STR to STCP; see Figure 14								
		V _{CC} = 3.0 V to 3.6 V	4.6	-	-	5.3	-	5.8	-	ns
V _{CC} = 4.5 V to 5.5 V	3.2	-	-	3.7	-	4.3	-	ns		
C _{PD}	power dissipation capacitance	f _i = 1 MHz; V _I = GND to V _{CC} [2]	-	55	-	-	-	-	-	pF
74AHCT594-Q100; V _{CC} = 4.5 V to 5.5 V										
t _{PLH}	LOW to HIGH propagation delay	SHCP to Q7S; see Figure 9								
		C _L = 15 pF	-	3.8	6.3	1.7	7.2	1.7	7.8	ns
		C _L = 50 pF	-	4.8	8.0	2.2	9.1	2.2	9.9	ns
		STCP to Qn; see Figure 10								
		C _L = 15 pF	-	3.5	5.7	1.8	6.5	1.8	7.1	ns
C _L = 50 pF	-	4.6	7.7	2.6	8.8	2.6	9.6	ns		

Table 7. Dynamic characteristics ...continuedVoltages are referenced to GND (ground = 0 V); for test circuit see [Figure 15](#).

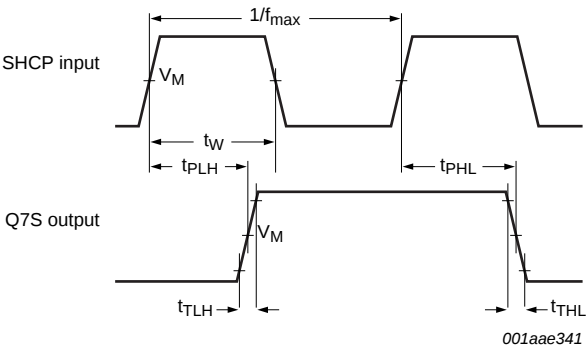
Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Typ ^[1]	Max	Min	Max	Min	Max	
t _{PHL}	HIGH to LOW propagation delay	SHCP to Q7S; see Figure 9								
		C _L = 15 pF	-	4.1	6.7	1.8	7.6	1.8	8.3	ns
		C _L = 50 pF	-	5.4	8.8	2.4	10.1	2.4	11.0	ns
		STCP to Q _n ; see Figure 10								
		C _L = 15 pF	-	3.7	6.1	1.9	6.9	1.9	7.2	ns
		C _L = 50 pF	-	5.2	8.5	2.6	9.7	2.6	10.5	ns
		SHR to Q7S; see Figure 13								
		C _L = 15 pF	-	4.3	7.0	2.4	8.0	2.4	8.7	ns
		C _L = 50 pF	-	5.4	8.8	2.7	10.1	2.7	11.0	ns
f _{max}	maximum frequency	STR to Q _n ; see Figure 14								
		C _L = 15 pF	-	4.5	7.4	2.3	8.4	2.3	9.2	ns
		C _L = 50 pF	-	5.7	9.4	3.1	10.7	3.1	11.7	ns
t _{max}	maximum frequency	SHCP or STCP; see Figure 9 and Figure 10	90	160	-	80	-	70	-	MHz
t _W	pulse width	SHCP and STCP HIGH or LOW; see Figure 9 and Figure 10	5.5	-	-	6.0	-	6.5	-	ns
		SHR and STR HIGH or LOW; see Figure 13 and Figure 14	5.2	-	-	5.5	-	6.0	-	ns
t _{su}	set-up time	DS to SHCP; see Figure 11	3.0	-	-	3.0	-	3.5	-	ns
		SHR to STCP; see Figure 12	5.0	-	-	5.0	-	5.5	-	ns
		SHCP to STCP; see Figure 10	5.0	-	-	5.0	-	5.5	-	ns
t _h	hold time	DS to SHCP; see Figure 11	2.0	-	-	2.0	-	2.5	-	ns
t _{rec}	recovery time	SHR to SHCP; see Figure 13	2.9	-	-	3.3	-	3.8	-	ns
		STR to STCP; see Figure 14	3.4	-	-	3.8	-	4.3	-	ns
C _{PD}	power dissipation capacitance	f _i = 1 MHz; V _I = GND to V _{CC} ^[2]	-	55	-	-	-	-	-	pF

[1] Typical values are measured at nominal supply voltage (V_{CC} = 3.3 V and V_{CC} = 5.0 V).[2] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).P_D = C_{PD} × V_{CC}² × f_i × N + Σ(C_L × V_{CC}² × f_o) where:f_i = input frequency in MHz;f_o = output frequency in MHz;C_L = output load capacitance in pF;V_{CC} = supply voltage in V;

N = number of inputs switching;

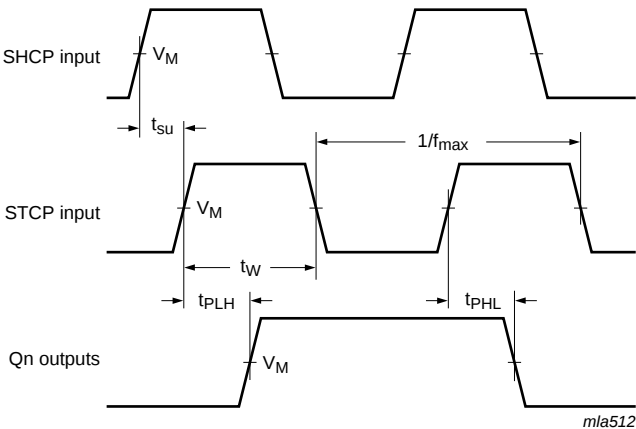
Σ(C_L × V_{CC}² × f_o) = sum of the outputs.

12. Waveforms



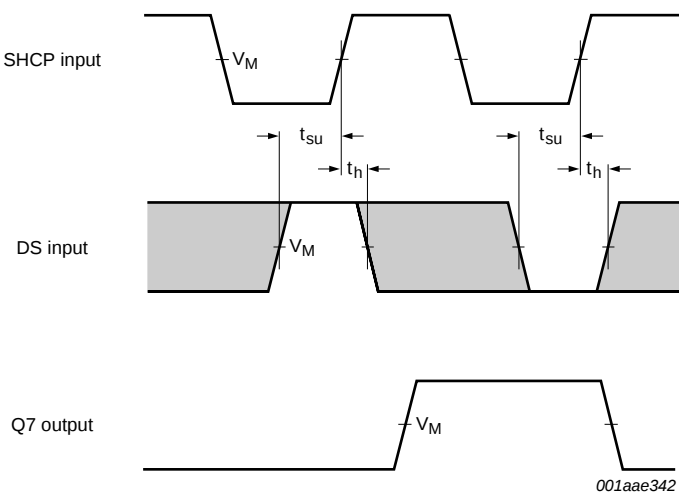
Measurement points are given in [Table 8](#).

Fig 9. Shift register clock pulse width, maximum frequency and input to output propagation delays



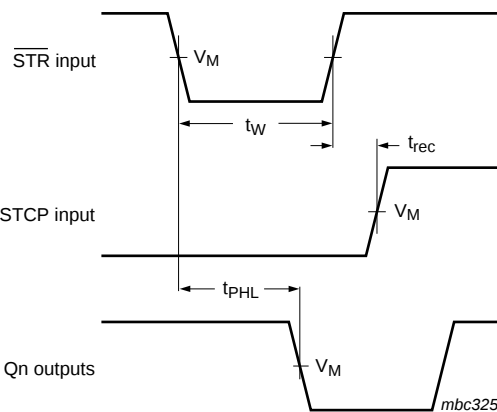
Measurement points are given in [Table 8](#).

Fig 10. Shift register clock to storage register clock set-up time and storage clock pulse width, maximum frequency and input to output propagation delays

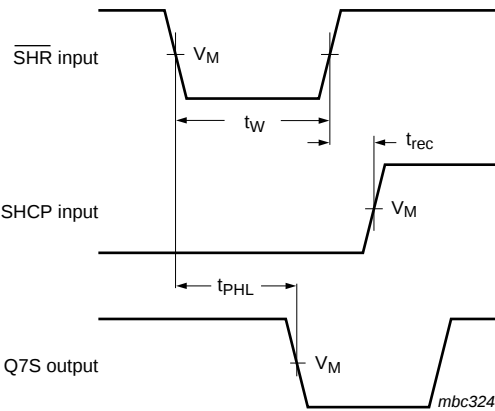


Measurement points are given in [Table 8](#).
The shaded areas indicate when the input is permitted to change for predictable output performance.

Fig 11. Shift register clock to data input set-up and hold times

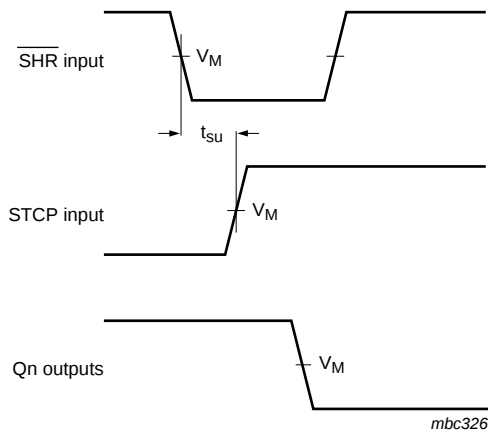


Measurement points are given in [Table 8](#).
Fig 12. Storage register reset pulse width, input to output propagation delay and recovery time



Measurement points are given in [Table 8](#).

Fig 13. Shift register reset pulse width, input to output propagation delay and recovery time

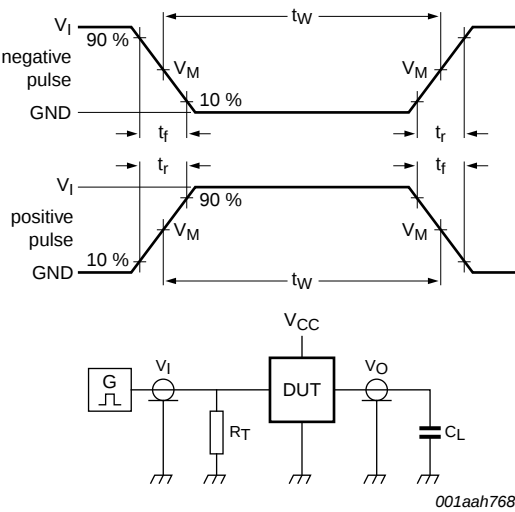


Measurement points are given in [Table 8](#).

Fig 14. Shift register reset to storage register clock set-up time

Table 8. Measurement points

Type	Input	Output
	V_M	V_M
74AHC594-Q100	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
74AHCT594-Q100	1.5 V	$0.5 \times V_{CC}$



For test data, see [Table 9](#).

Definitions for test circuit:

R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.

C_L = Load capacitance including jig and probe capacitance.

Fig 15. Load circuitry for measuring switching times

Table 9. Test data

Type	Input		Load	Test
	V_I	t_r, t_f	C_L	
74AHC594-Q100	V_{CC}	$\leq 3.0 \text{ ns}$	15 pF, 50 pF	t_{PLH}, t_{PHL}
74AHCT594-Q100	3.0 V	$\leq 3.0 \text{ ns}$	15 pF, 50 pF	t_{PLH}, t_{PHL}

13. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1

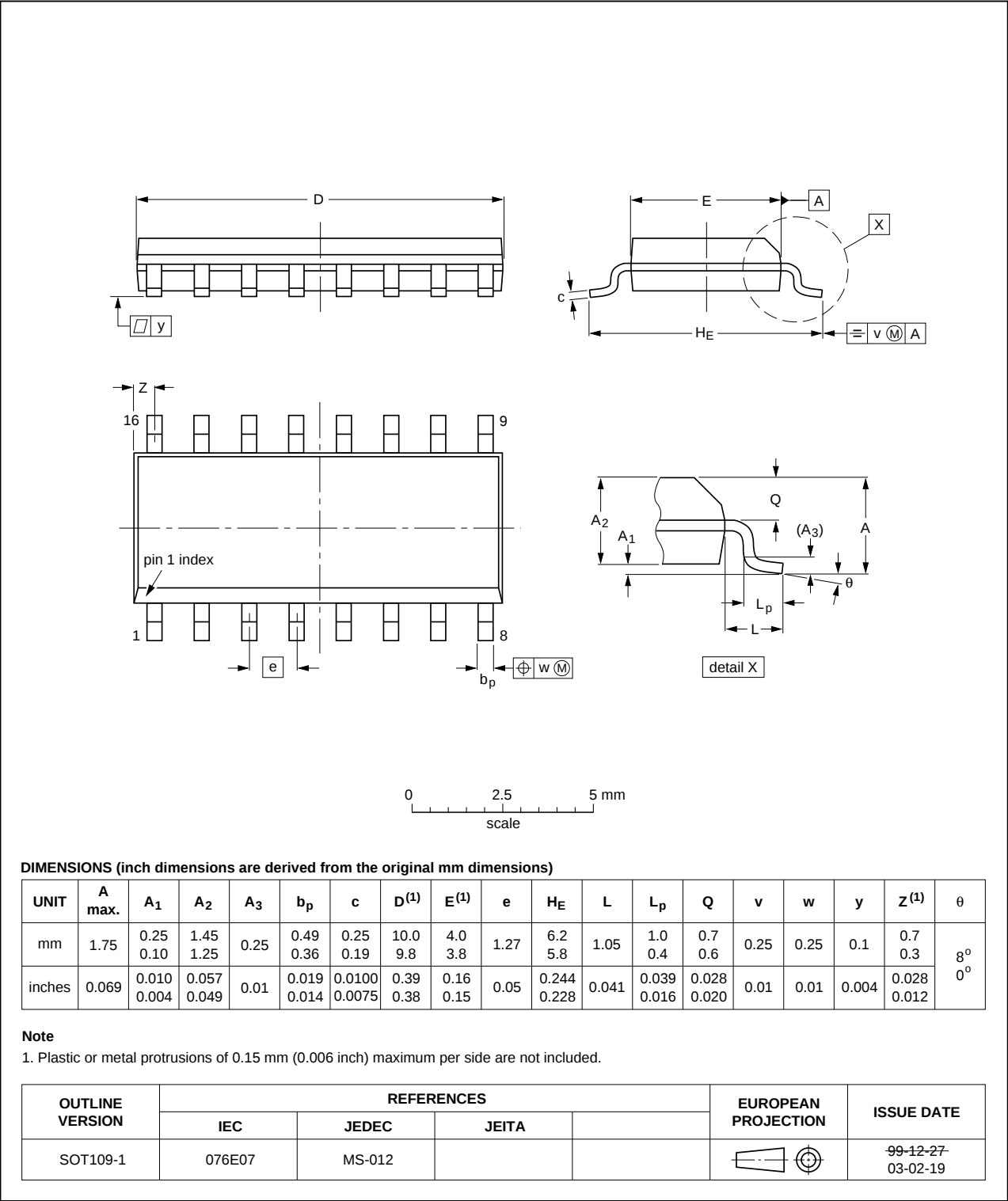


Fig 16. Package outline SOT109-1 (SO16)

SSOP16: plastic shrink small outline package; 16 leads; body width 5.3 mm

SOT338-1

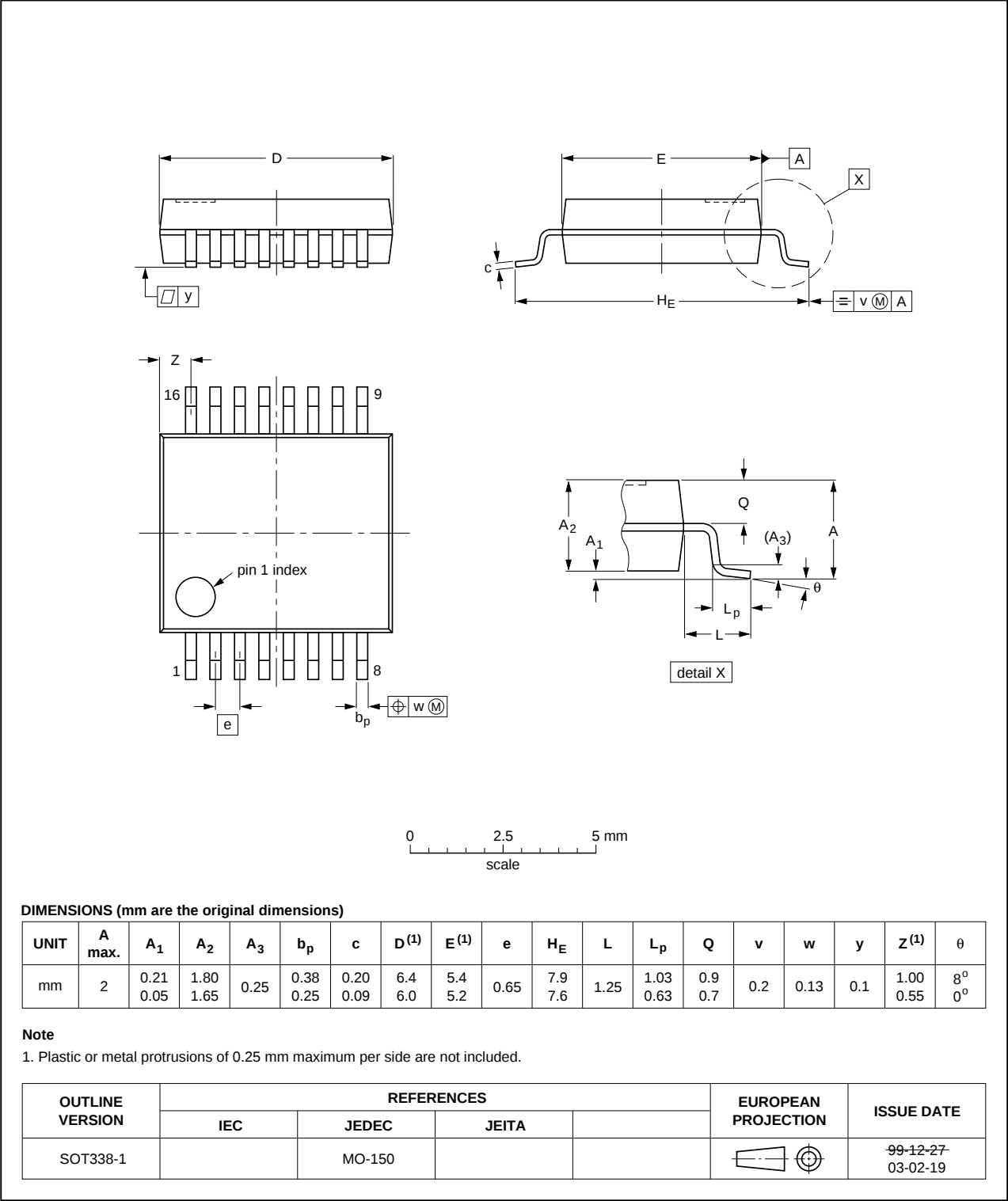


Fig 17. Package outline SOT338-1 (SSOP16)

TSSOP16: plastic thin shrink small outline package; 16 leads; body width 4.4 mm

SOT403-1

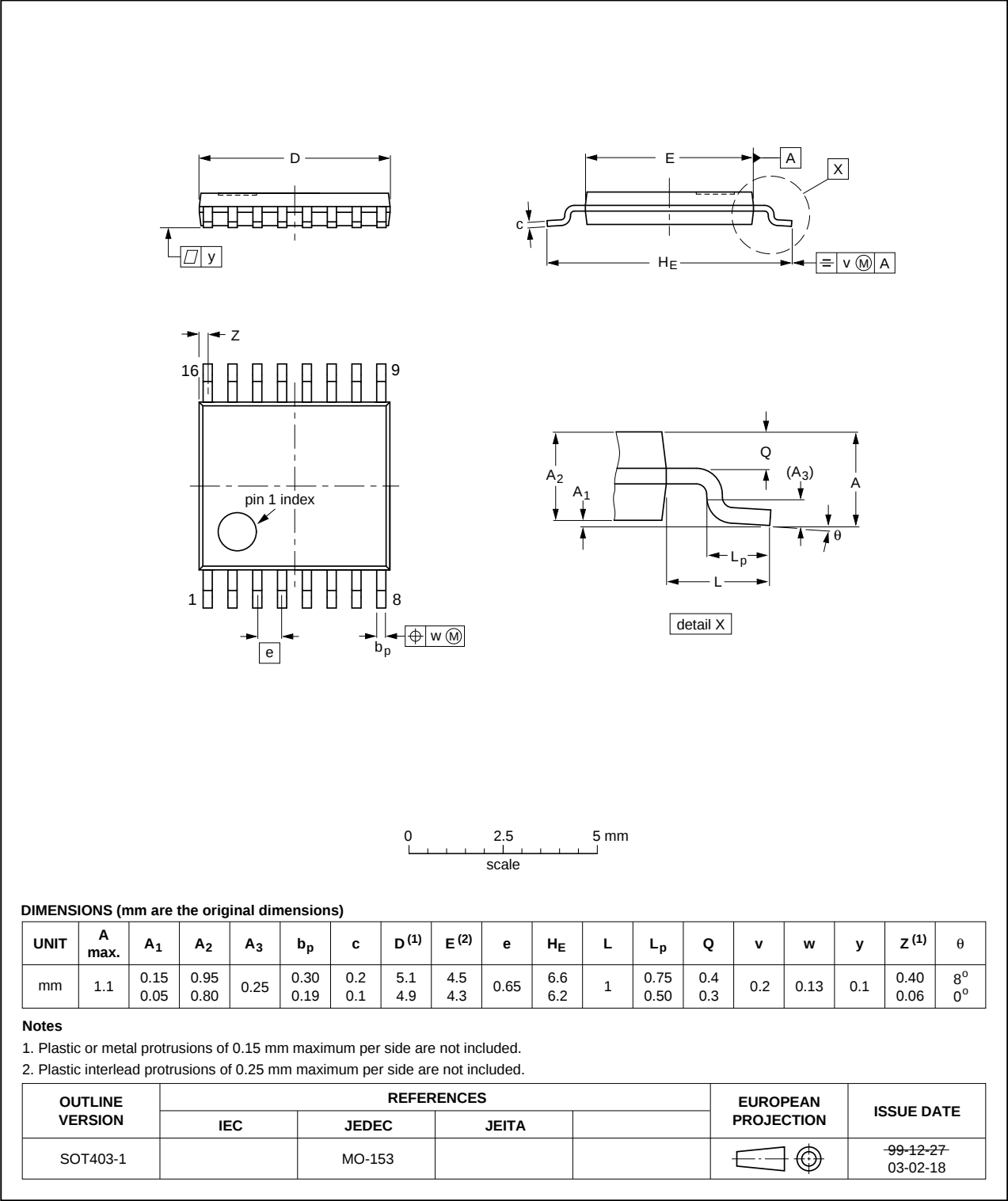


Fig 18. Package outline SOT403-1 (TSSOP16)

DHVQFN16: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 16 terminals; body 2.5 x 3.5 x 0.85 mm SOT763-1

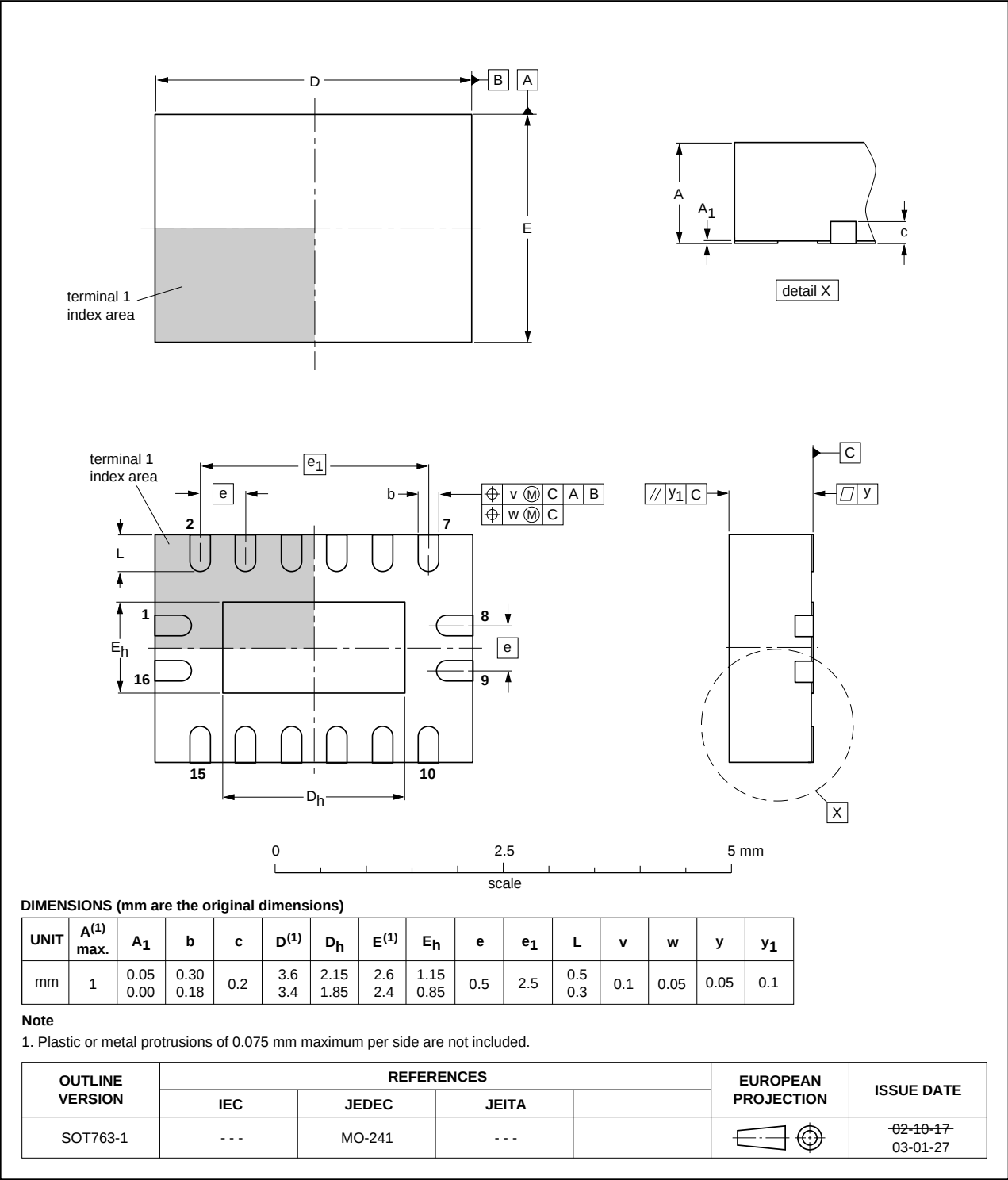


Fig 19. Package outline SOT763-1 (DHVQFN16)

14. Abbreviations

Table 10. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
LSTTL	Low-power Schottky Transistor-Transistor Logic
MM	Machine Model
MIL	Military

15. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHC_AHCT594_Q100 v.2	20130704	Product data sheet	-	74AHC_AHCT594_Q100 v.1
Modifications: • 74AHC594DB-Q100 and 74AHCT594DB-Q100 added.				
74AHC_AHCT594_Q100 v.1	20120712	Product data sheet	-	-

16. Legal information

16.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

16.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

16.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use in automotive applications — This Nexperia product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

16.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

17. Contact information

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

18. Contents

1	General description	1
2	Features and benefits	1
3	Applications	2
4	Ordering information	2
5	Functional diagram	3
6	Pinning information	4
6.1	Pinning	4
6.2	Pin description	5
7	Functional description	6
8	Limiting values	7
9	Recommended operating conditions	8
10	Static characteristics	8
11	Dynamic characteristics	9
12	Waveforms	13
13	Package outline	17
14	Abbreviations	21
15	Revision history	21
16	Legal information	22
16.1	Data sheet status	22
16.2	Definitions	22
16.3	Disclaimers	22
16.4	Trademarks	23
17	Contact information	23
18	Contents	24