VE890 SERIES

PRODUCT PREVIEW

The VE8910 , VE8910-HV, VE8901, VE8911, VE8911-HV and VE8921 chipsets are highly integrated, cost-effective FXS, FXO and FXS + FXO chipsets that are optimized for residential VoIP access devices. These chipsets implements a complete BORSCHT functionality by providing the necessary voice interface functions to connect to, and power, one or more telephones and an isolated Direct Access Arrangement (DAA) for connection to the PSTN. The VE890 Series chipsets significantly increase design flexibility, improve system performance, and reduce BOM costs. These chipsets are supported by the Zarlink VoicePath™ API-II™ (VP API-II), which enables designers to offer a single hardware design that is software programmable for worldwide markets.

VE890 Series		
Chipset	OPN	Description
1FXS		
VE8910	Le89116	SLAC
	Le89810	SLIC
VE8910-HV	Le89136	SLAC
	Le89830	SLIC
1FXO		
VE8901	Le89010	OLAC
	Le89900	DAA
1FXS + 1FXO		
VE8911	Le89316	SOLAC
	Le89810	SLIC
	Le89900	DAA
VE8911-HV	Le89336	SOLAC
	Le89830	SLIC
	Le89900	DAA
2FXS + 1FXO		
VE8921	Le89316	SOLAC
	Le89116	SLAC
	Le89810	SLIC
	Le89810	SLIC
	Le89900	DAA

Features

- Highly integrated, low-cost 1FXS, 1FXO and 1FXS + 1FXO chipset optimized for residential VoIP gateways
- Full worldwide programmability, a single hardware design can be configured via software for use in all worldwide markets
- FXS channel performs all the standard BORSCHT functions
- Integrated power management minimizes power consumption, making the VE890 Series suitable for battery backed applications
- Full support for wideband audio with 16 kHz sampling
- FXO channel performs all PSTN interface functions with software programmable parameters
- Best in class DAA performance in common mode noise rejection and RF immunity
- Unique adaptive hybrid balance feature for impedance matching reduces local echo and improves voice quality
- DAA isolation using no-cost PCB capacitors reduces RBOM cost
- Integrated VoicePath™ API-II Software implements all FXS & FXO functions, significantly reducing development time and providing one common interface for FXS and FXO functions
- Comprehensive line sensing and line monitoring with VeriVoice™ Test Suite software—provides complete GR-909/TIA-1063 diagnostics, loop test and supervision functions
- Direct FXO to FXS voice path eliminates processor buffering overhead
- Package offerings support board stuffing options for popular voice configurations—1FXS, 1FXO, 2FXS, 1FXS + 1FXO and 2FXS + 1FXO

Applications

- Residential and SOHO VoIP CPE, such as ADSL2+/VDSL2 Integrated Access Devices (IADs)
- Residential VoIP Gateways and Routers

ZARLINK SEMICONDUCTOR

VE890 SERIES

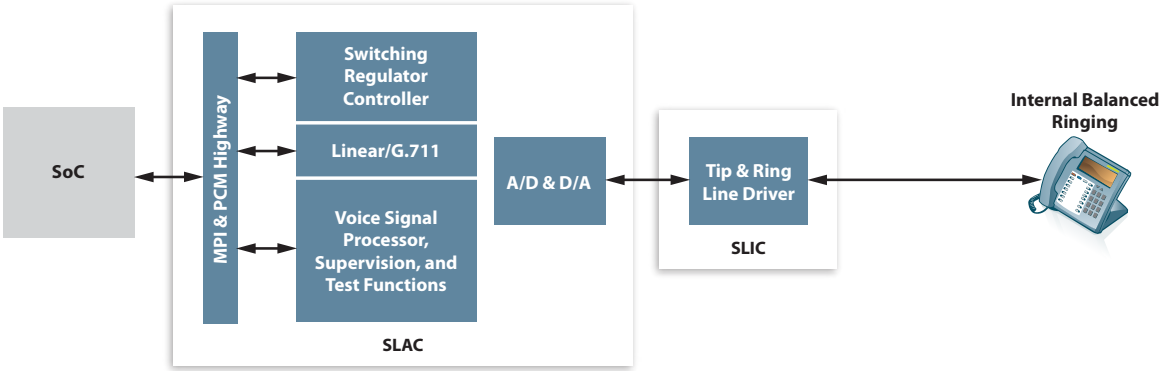
APPLICATION

Zarlink provides a world-class suite of development tools to facilitate the integration of Legerity voice solutions into end-user applications and to significantly reduce design time.

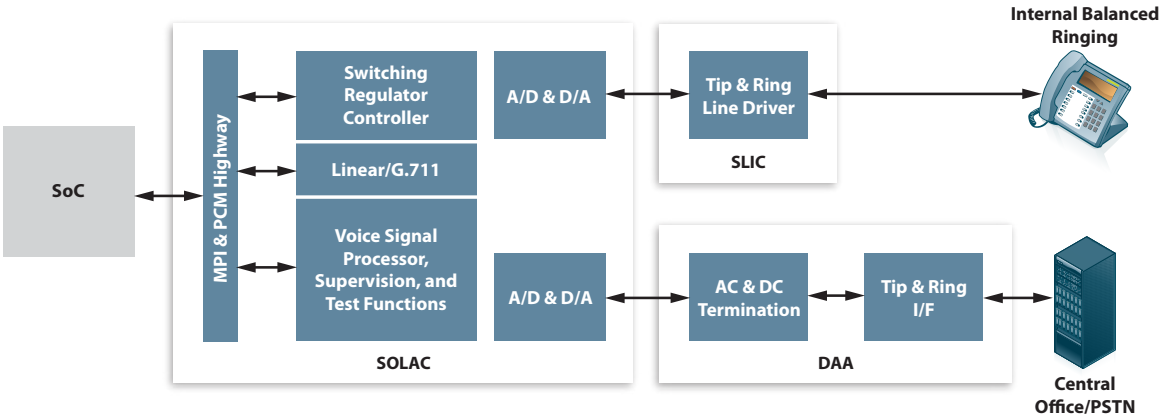
The VoicePath API-II™ (VP API-II) is a set of 'C' code used to abstract devices from application code while providing functions for controlling, supervising, packet exchanging, and testing a set of subscriber lines. It is application and OS independent in that it requires no memory (only memory required is provided by application space) and supports reentrancy. It has been used in Linux (application and kernel space), VxWorks, and non-OS architectures. The API-II is common across all Legerity voice solutions so applications written for one device are the same for other devices. It integrates seamlessly with the Zarlink's Legerity Software Development Kit (SDK) Tools so your development time is significantly reduced.

The VeriVoice™ Test Suite is a subscriber line software package for VoIP equipment, providing the market's most cost effective and reliable solution for VoIP line test while at the same time minimizing the cost of ownership for service providers. Self-test available with VeriVoice Professional.

VE8910 or VE8910-HV Chipset—1 Channel FXS



VE8911 or VE8911-HV Chipset—1 Channel FXS + 1 Channel FXO



FEATURING

Legerity

VOICE SOLUTIONS

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