

RJM0603JSC

Silicon N/P Channel Power MOS FET (6 in 1 Type) High Speed Power Switching

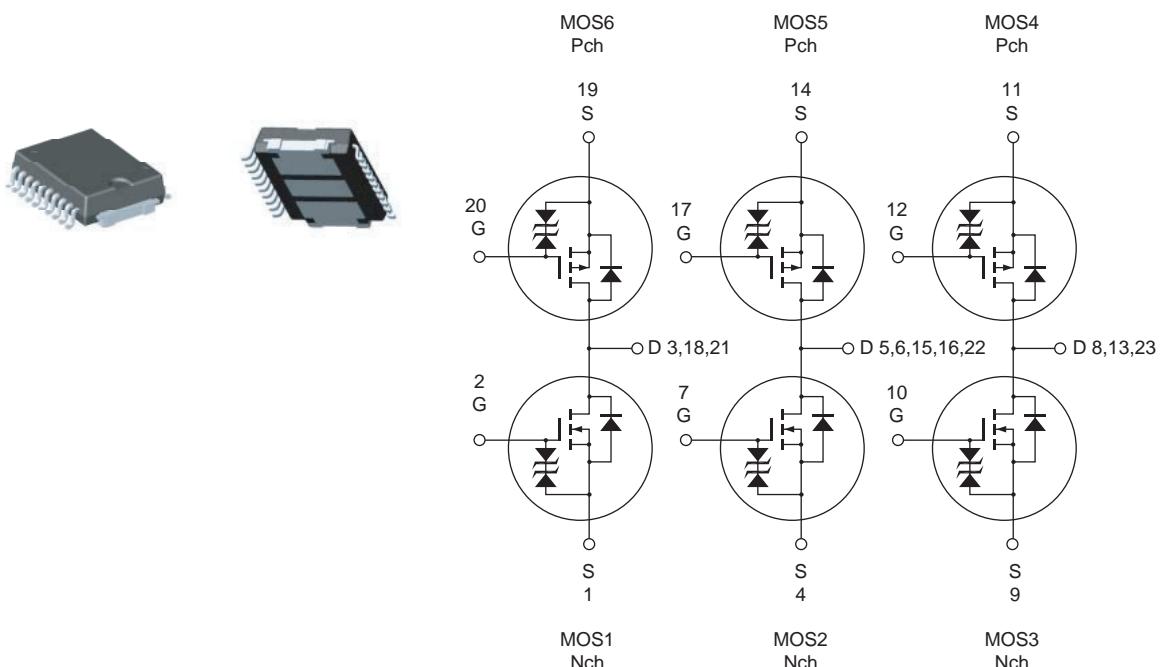
R07DS0339EJ0501
Rev.5.01
Jul 22, 2011

Features

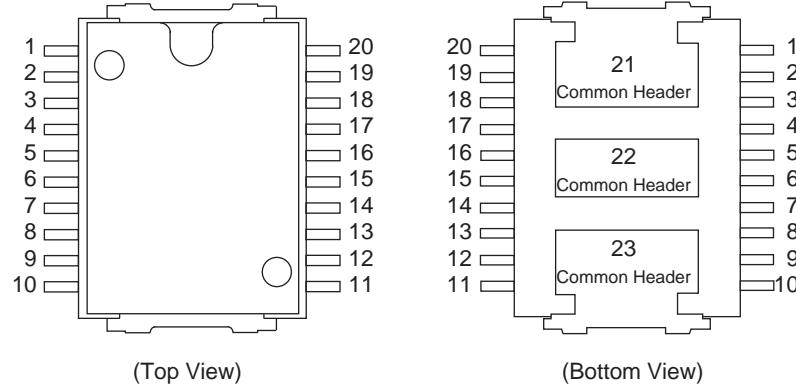
- For Automotive applications
- AEC-Q101 compliant
- N/P Channel MOS FET (6 in 1 Type). High density mounting
- Low on-resistance
- Capable of 4.5 V gate drive

Outline

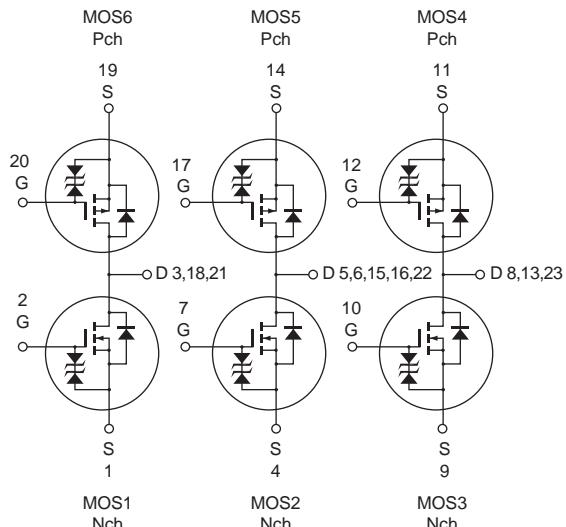
RENESAS Package Code: PRSP0020DF-A
(Package Name: HSOP-20)



Pin Arrangement



No.		
1	MOS1	Source
2	MOS1	Gate
3, 18	MOS1, 6	Drain
4	MOS2	Source
5, 6, 15, 16	MOS2, 5	Drain
7	MOS2	Gate
8, 13	MOS3, 4	Drain
9	MOS3	Source
10	MOS3	Gate
11	MOS4	Source
12	MOS4	Gate
14	MOS5	Source
17	MOS5	Gate
19	MOS6	Source
20	MOS6	Gate
21	MOS1, 6	Drain (Header)
22	MOS2, 5	Drain (Header)
23	MOS3, 4	Drain (Header)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value		Unit
		MOS1, 2, 3 (Nch)	MOS4, 5, 6 (Pch)	
Drain to source voltage	V _{DSS}	60	-60	V
Gate to source voltage	V _{GSS}	+20 / -5	-20 / +5	V
Drain current	I _D	20	-20	A
Drain peak current	I _D (pulse) ^{Note1}	80	-80	A
Channel dissipation	P _{ch} ^{Note2}	54	54	W
Avalanche current	I _{AP} ^{Note3}	20	20	A
Avalanche energy	E _{AR} ^{Note3}	34	34	mJ
Channel temperature	T _{ch} ^{Note4}	175	175	°C
Storage temperature	T _{stg}	-55 to +150	-55 to +150	°C

Notes:

1. PW ≤ 10µs duty cycle ≤ 1%
2. 1 Drive Operation ; Value at T_c = 25°C
3. Value at T_{ch} = 25°C, R_g ≥ 50 Ω
4. AEC-Q101 compliant

Thermal Impedance Characteristics

- Channel to case thermal impedance θ_{ch-c}: 2.78°C/W

Electrical Characteristics

• MOS1, MOS2, MOS3 (N Channel)

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage drain current	I _{DSS}	—	—	10	μA	V _{DS} = 60 V, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = +20 V / -5 V, V _{DS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.0	—	2.5	V	V _{DS} = 10 V, I _D = 1 mA
Static drain to source on state resistance	R _{DS(on)}	—	16	20	mΩ	I _D = 10 A, V _{GS} = 10 V ^{Note5}
		—	21	32	mΩ	I _D = 10 A, V _{GS} = 4.5 V ^{Note5}
		—	—	—	—	—
Input capacitance	C _{iss}	—	2600	—	pF	V _{DS} = 10V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	290	—	pF	
Reverse transfer capacitance	C _{rss}	—	140	—	pF	
Total gate charge	Q _g	—	43	—	nC	V _{DD} = 25 V, V _{GS} = 10 V, I _D = 20 A
Gate to source charge	Q _{gs}	—	6.2	—	nC	
Gate to drain charge	Q _{gd}	—	7.2	—	nC	
Turn-on delay time	t _{d(on)}	—	13	—	ns	
Rise time	t _r	—	6	—	ns	V _{GS} = 10 V, I _D = 10 A, V _{DD} ≈ 30 V, R _L = 3 Ω, R _G = 4.7 Ω
Turn-off delay time	t _{d(off)}	—	65	—	ns	
Fall time	t _f	—	4.5	—	ns	
Body-drain diode forward voltage	V _{DF}	—	0.91	1.18	V	I _F = 20 A, V _{GS} = 0 ^{Note5}
Body-drain diode reverse recovery time	t _{rr}	—	35	—	ns	I _F = 20 A, V _{GS} = 0 di _F /dt = 100 A/μs

Note: 5. Pulse test

• MOS4, MOS5, MOS6 (P Channel)

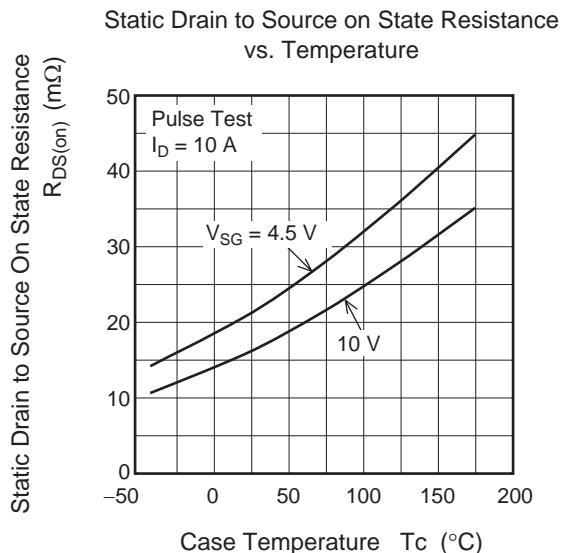
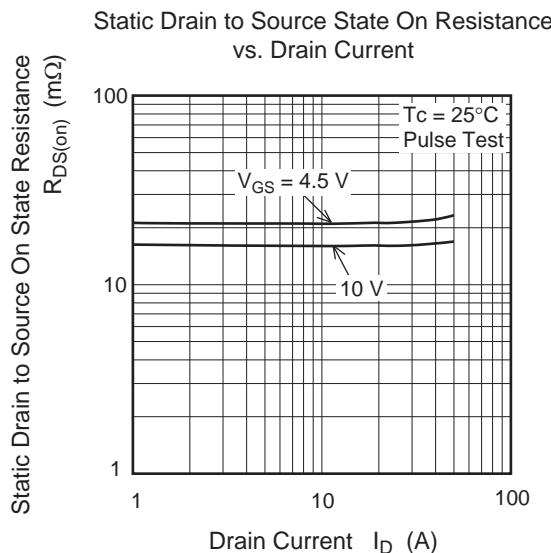
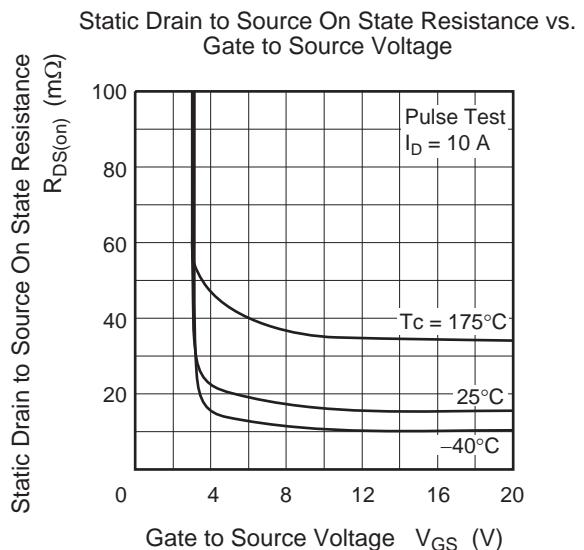
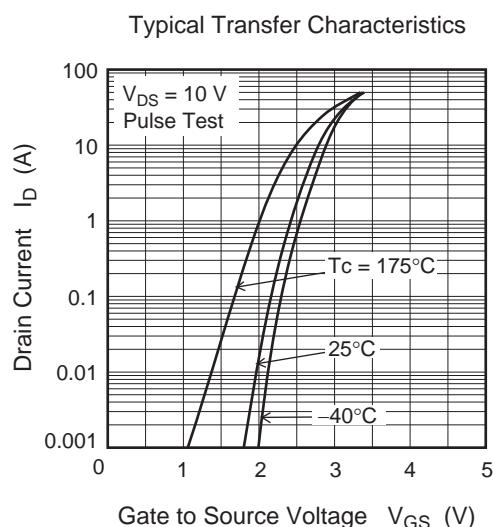
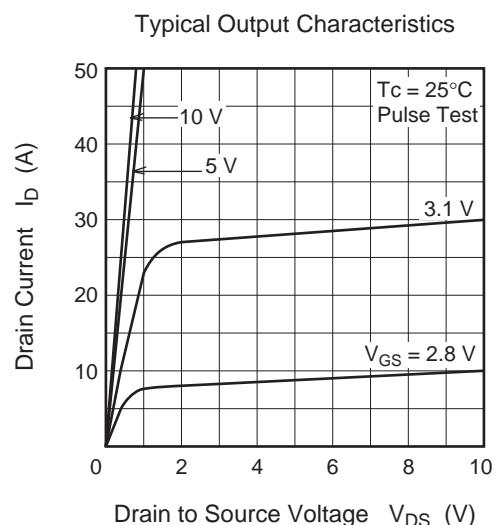
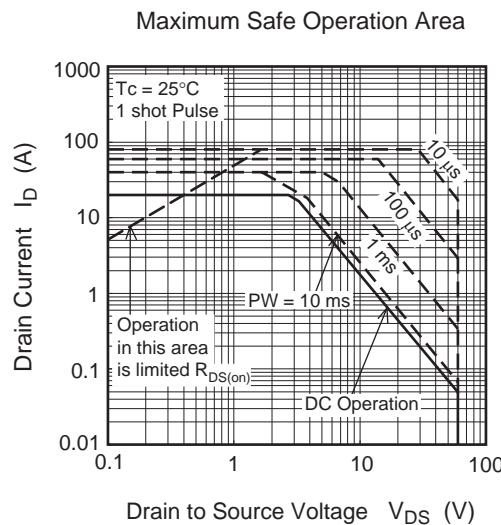
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero gate voltage drain current	I _{DSS}	—	—	-10	μA	V _{DS} = -60 V, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = -20 V / +5 V, V _{DS} = 0
Gate to source cutoff voltage	V _{GS(off)}	-1.0	—	-2.5	V	V _{DS} = -10 V, I _D = -1 mA
Static drain to source on state resistance	R _{DS(on)}	—	32	40	mΩ	I _D = -10 A, V _{GS} = -10 V ^{Note6}
		—	42	64	mΩ	I _D = -10 A, V _{GS} = -4.5 V ^{Note6}
Input capacitance	C _{iss}	—	2600	—	pF	V _{DS} = -10 V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	330	—	pF	
Reverse transfer capacitance	C _{rss}	—	240	—	pF	
Total gate charge	Q _g	—	53	—	nC	V _{DD} = -25 V, V _{GS} = -10 V, I _D = -20 A
Gate to source charge	Q _{gs}	—	8.8	—	nC	
Gate to drain charge	Q _{gd}	—	13	—	nC	
Turn-on delay time	t _{d(on)}	—	22	—	ns	
Rise time	t _r	—	17	—	ns	V _{GS} = -10 V, I _D = -10 A, V _{DD} ≈ -30 V, R _L = 3 Ω, R _G = 4.7 Ω
Turn-off delay time	t _{d(off)}	—	100	—	ns	
Fall time	t _f	—	20	—	ns	
Body-drain diode forward voltage	V _{DF}	—	-0.95	-1.24	V	I _F = -20 A, V _{GS} = 0 ^{Note6}
Body-drain diode reverse recovery time	t _{rr}	—	50	—	ns	I _F = -20 A, V _{GS} = 0 di _F /dt = 100 A/μs

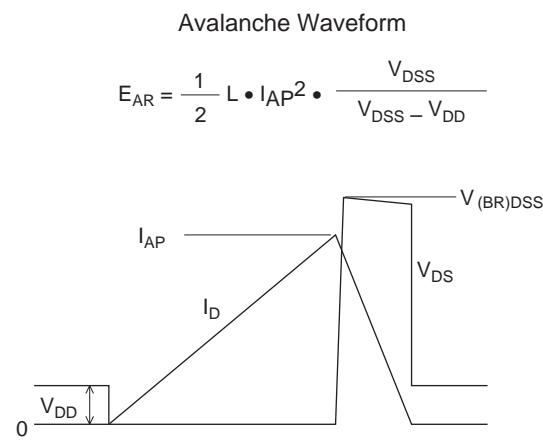
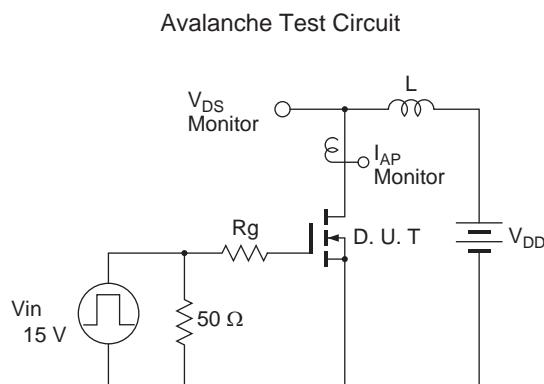
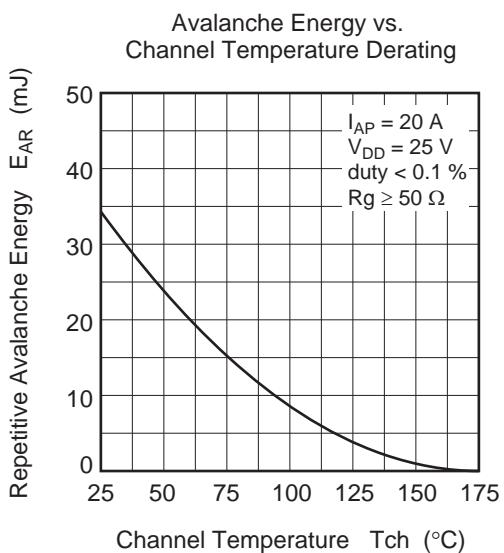
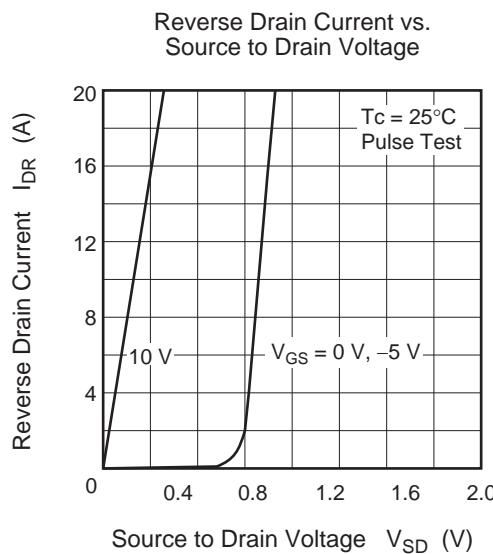
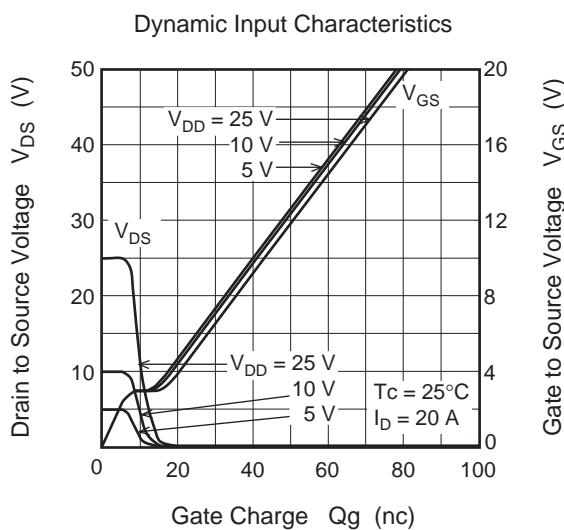
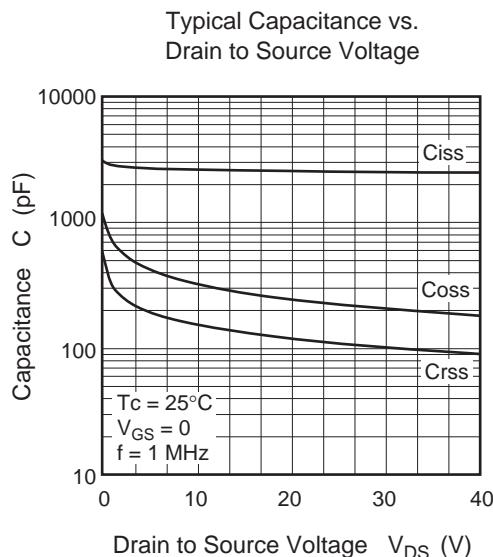
Note: 6. Pulse test

Main Characteristics

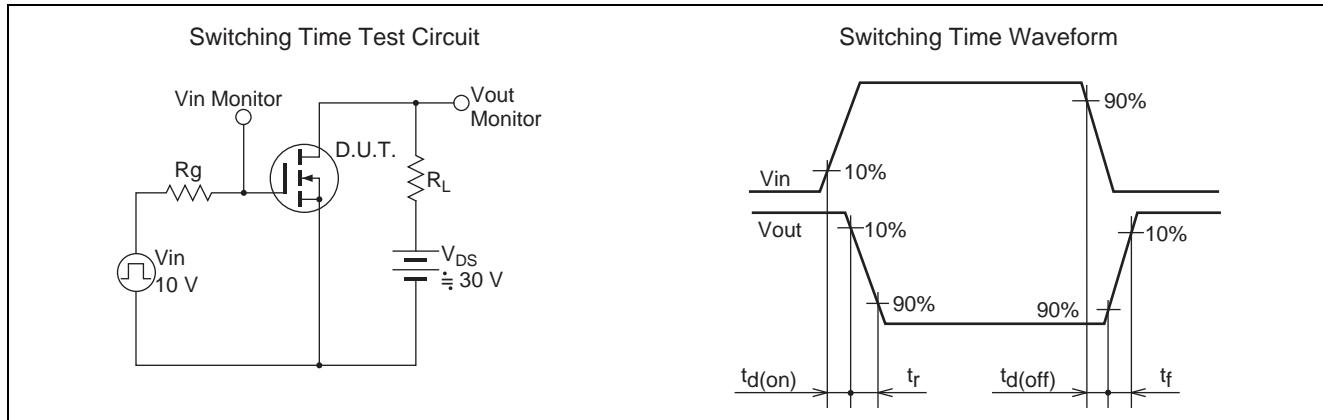
- MOS1, MOS2, MOS3 (N Channel)



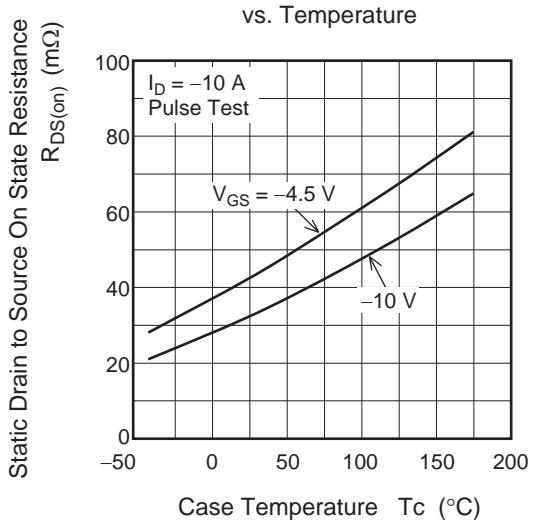
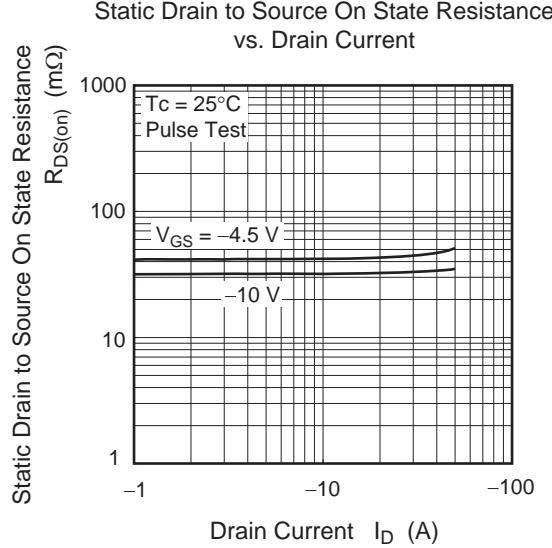
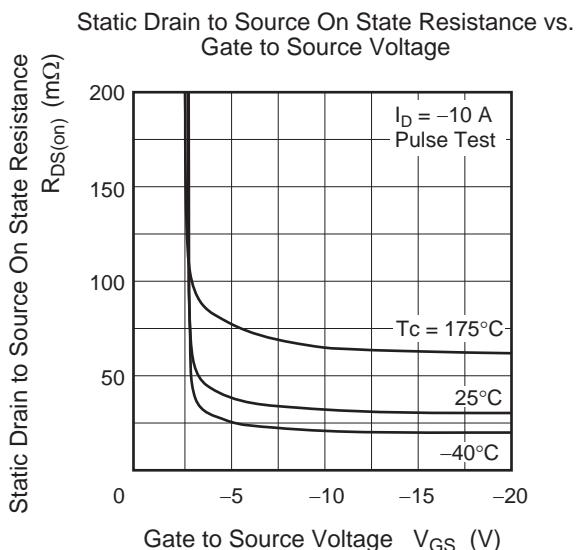
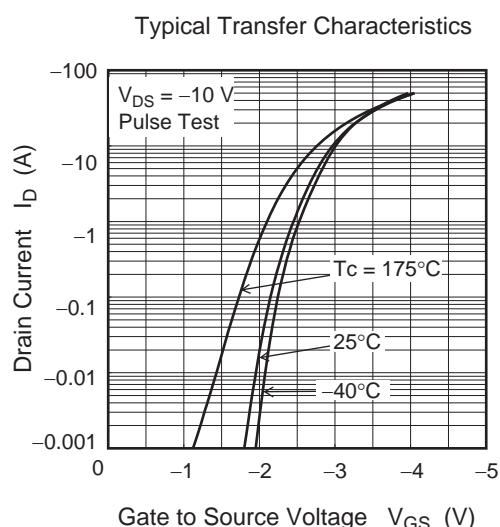
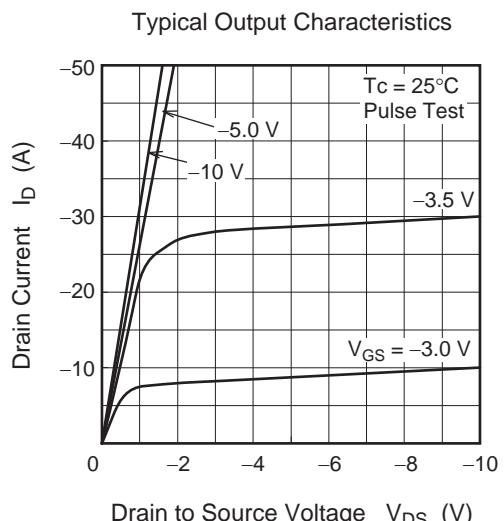
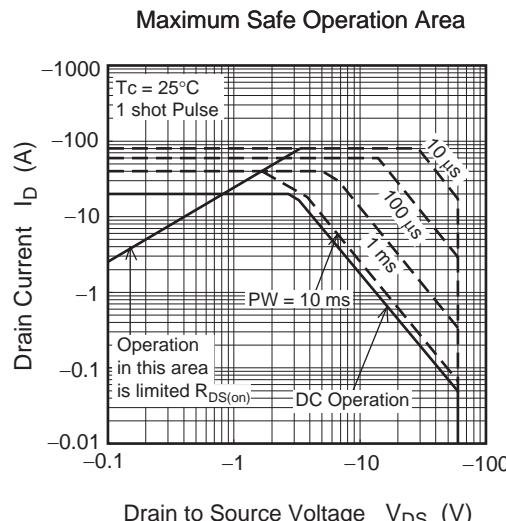
• MOS1, MOS2, MOS3 (N Channel)



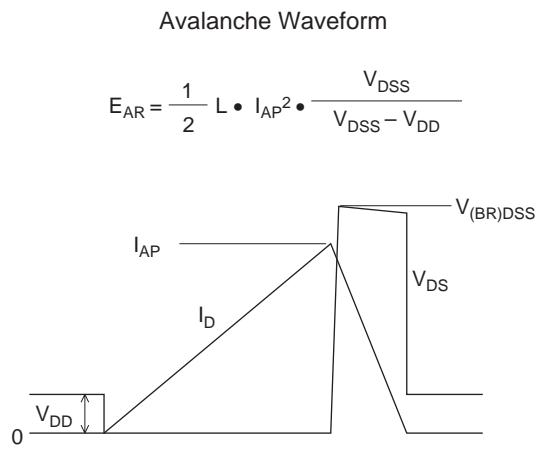
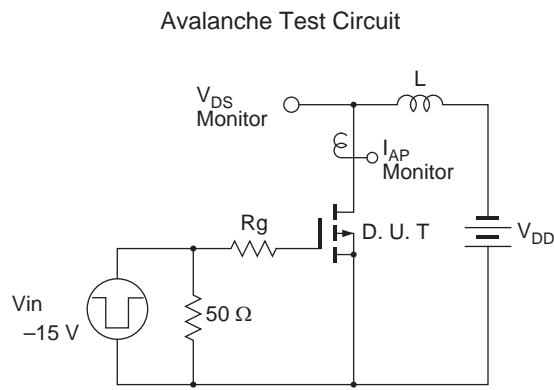
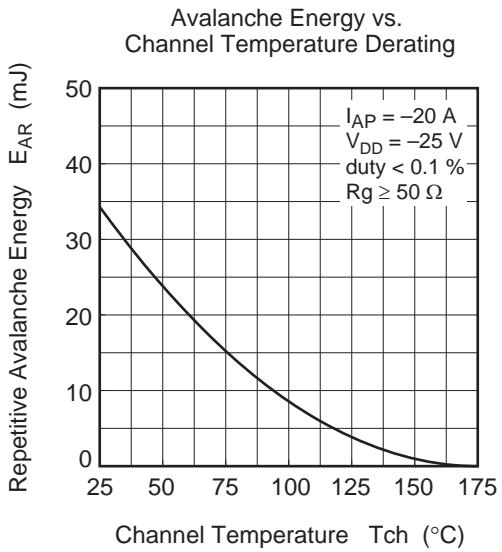
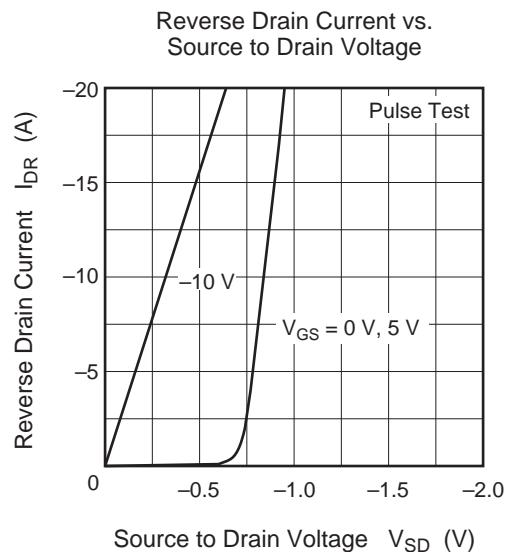
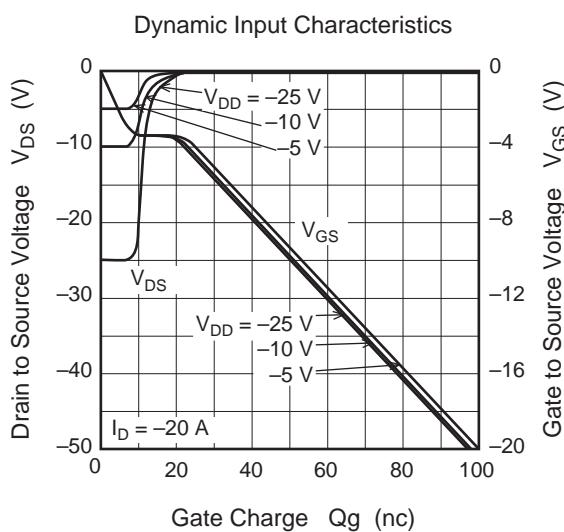
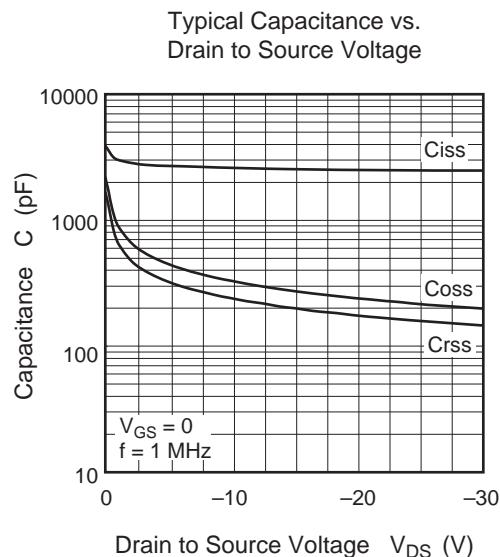
• MOS1, MOS2, MOS3 (N Channel)



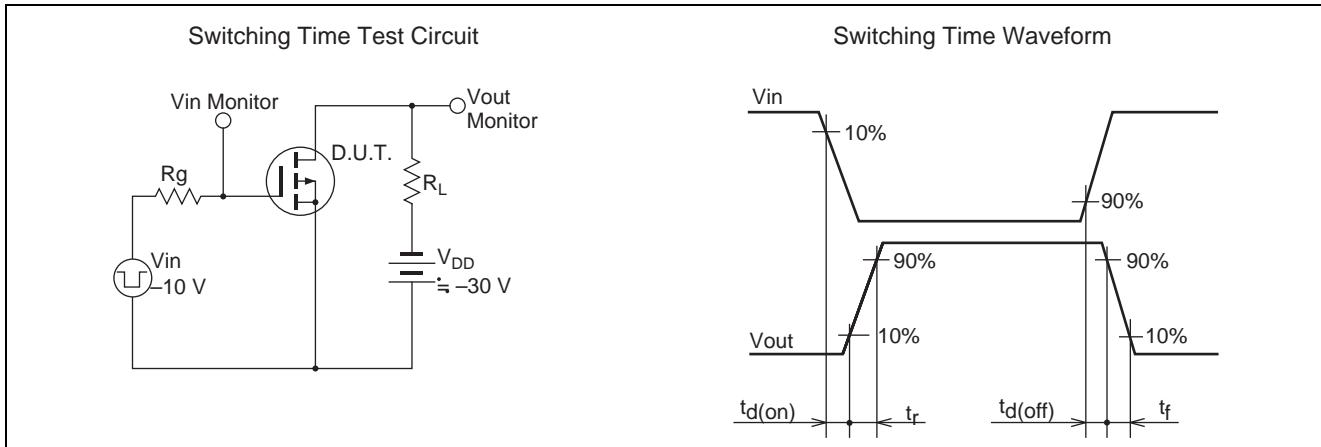
- MOS4, MOS5, MOS6 (P Channel)



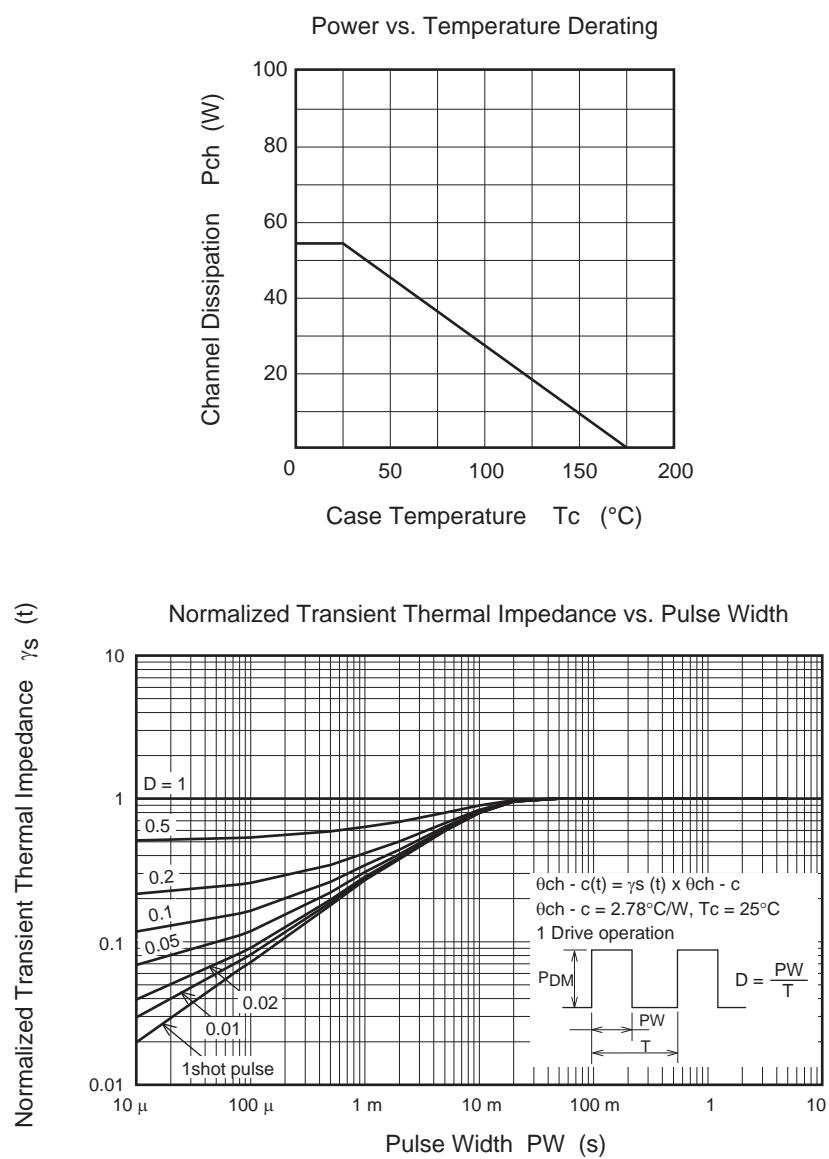
• MOS4, MOS5, MOS6 (P Channel)



• MOS4, MOS5, MOS6 (P Channel)



- Common



Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-HSOP20-11x14.1-1.27	PRSP0020DF-A	—	2.0g

NOTE)
1. DIMENSIONS "1" AND "2" DO NOT INCLUDE MOLD FLASH.
2. DIMENSION "3" DOES NOT INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	13.95	14.1	14.25
E	10.9	11.0	11.1
A ₂	—	—	—
A ₁	0.01	0.05	0.10
A	—	—	3.6
b _p	0.40	0.47	0.53
b ₁	—	0.45	—
c	0.28	0.32	0.37
c ₁	—	0.30	—
θ	0°	—	8°
H _E	14.0	14.2	14.4
⊕	—	1.27	—
x	—	—	0.25
y	—	—	0.1
z	—	—	1.8
L	0.65	0.8	0.95
L ₁	—	—	—

Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJM0603JSC-00-12	700 pcs	Tray

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