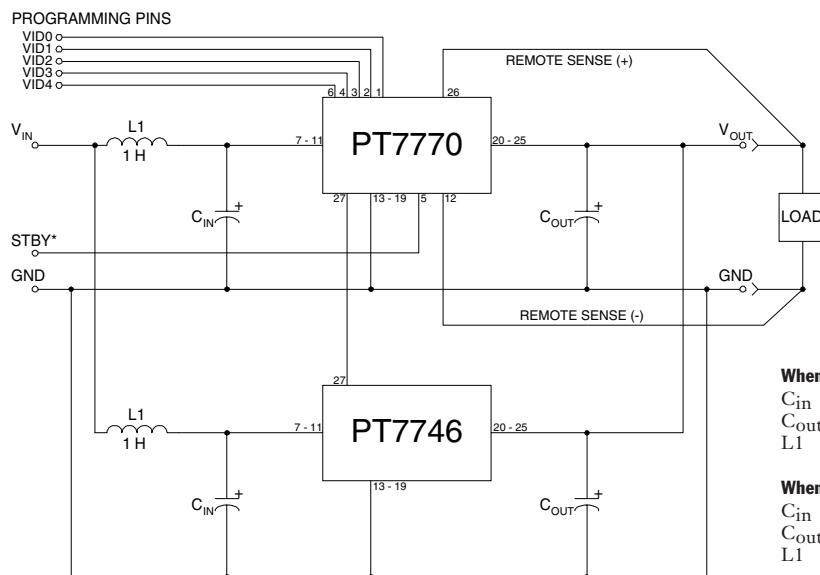


Features

- 32A Current Boost
- Tracks Vo of a PT7770
- High Efficiency
- Input Voltage Range: 3V to 5.5V
- Synchronized with PT7770
- 27-pin SIP Package
- Run up to 2 in Parallel - 96 Amps

Standard Application



Output Capacitors: When used with a PT7771 or PT7772, the PT7746 requires a minimum output capacitance of 2400 μ F. When used with a PT7777, the PT7746 requires a minimum output capacitance of 680 μ F for proper operation. Do not use Oscon type capacitors. The maximum allowable output capacitance is 30,000 μ F.

Input Filter: An input filter is optional for most applications. The input inductor must be sized to handle 32ADC with a typical value of 1 μ H. The input capacitance must be rated for a minimum of 2.6Arms of ripple current. For transient or dynamic load applications, additional capacitance may be required.

regulator. As such, the system runs in perfect synchronization providing a low noise solution.

The PT7746 only operates in combination with a PT7770 series regulator and is not a stand-alone product. Please refer to the PT7771, PT7772, or PT7777 series data sheet for performance specifications.

The PT7746 has the same mechanical outline and package options as the PT7770 series.

Pin-Out Information

Pin	Function	Pin	Function
1	Do not connect	14	GND
2	Do not connect	15	GND
3	Do not connect	16	GND
4	Do not connect	17	GND
5	Do not connect	18	GND
6	Do not connect	19	GND
7	V _{in}	20	V _{out}
8	V _{in}	21	V _{out}
9	V _{in}	22	V _{out}
10	V _{in}	23	V _{out}
11	V _{in}	24	V _{out}
12	Do not connect	25	V _{out}
13	GND	26	Do not connect
		27	Master Sync In

Ordering Information

PT7746

(For dimensions and PC Board layout, see Package Styles 1020 and 1030.)

PT Series Suffix (PT1234X)

Case/Pin Configuration

Vertical Through-Hole	N
Horizontal Through-Hole	A
Horizontal Surface Mount	C

When used with PT7771/7772:

C_{in} = Required 2400 μ F electrolytic
C_{out} = Required 2400 μ F electrolytic
L₁ = Optional 1 μ H input choke

When used with PT7777:

C_{in} = Required 2400 μ F electrolytic
C_{out} = Required 680 μ F electrolytic
L₁ = Optional 1 μ H input choke

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
PT7746A	NRND	SIP MODULE	EJA	27		TBD	Call TI	Call TI	
PT7746C	NRND	SIP MODULE	EJC	27		TBD	Call TI	Call TI	
PT7746N	NRND	SIP MODULE	EJD	27		TBD	Call TI	Call TI	

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBsolete: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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