

# 74CBTLV3244

8-bit bus switch with 4-bit output enables

Rev. 3 — 8 November 2016

Product data sheet

## 1. General description

The 74CBTLV3244 is a dual 4-pole, single-throw bus switch. The device features two output enable inputs ( $nOE$ ) that each control four switch channels. The switches are disabled when the associated  $nOE$  input is HIGH. Schmitt trigger action at control inputs makes the circuit tolerant of slower input rise and fall times. This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

## 2. Features and benefits

- Supply voltage range from 2.3 V to 3.6 V
- High noise immunity
- Complies with JEDEC standard:
  - ◆ JESD8-5 (2.3 V to 2.7 V)
  - ◆ JESD8-B/JESD36 (2.7 V to 3.6 V)
- ESD protection:
  - ◆ HBM JESD22-A114F exceeds 2000 V
  - ◆ CDM AEC-Q100-011 revision B exceeds 1000 V
- 5  $\Omega$  switch connection between two ports
- Rail to rail switching on data I/O ports
- CMOS low power consumption
- Latch-up performance exceeds 250 mA per JESD78B Class I level A
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$  and  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$

**nexperia**

### 3. Ordering information

Table 1. Ordering information

Type number	Package	Temperature range	Name	Description	Version
74CBTLV3244DS	SSOP20 <sup>[1]</sup>	−40 °C to +125 °C		plastic shrink small outline package; 20 leads; body width 3.9 mm; lead pitch 0.635 mm	SOT724-1
74CBTLV3244PW	TSSOP20	−40 °C to +125 °C		plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1
74CBTLV3244BQ	DHVQFN20	−40 °C to +125 °C		plastic dual-in-line compatible thermal enhanced very thin quad flat package; no leads; 20 terminals; body 2.5 × 4.5 × 0.85 mm	SOT764-1

[1] Also known as QSOP20 package

### 4. Functional diagram

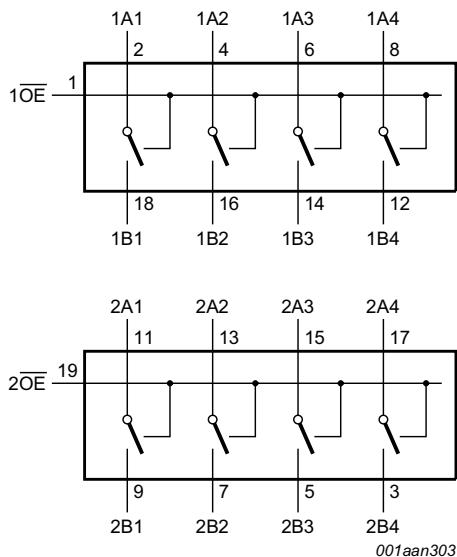


Fig 1. Logic symbol

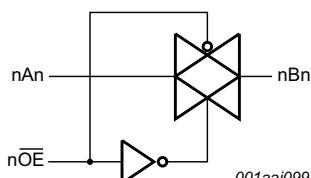


Fig 2. Logic diagram (one switch)

## 5. Pinning information

### 5.1 Pinning

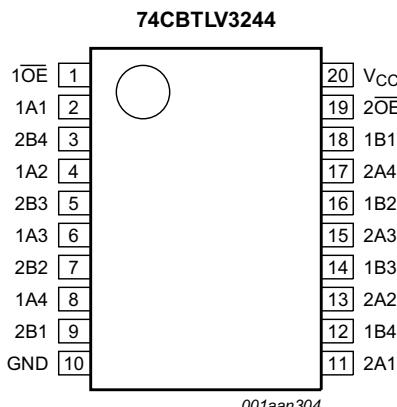


Fig 3. Pin configuration for TSSOP20 (SOT360-1)

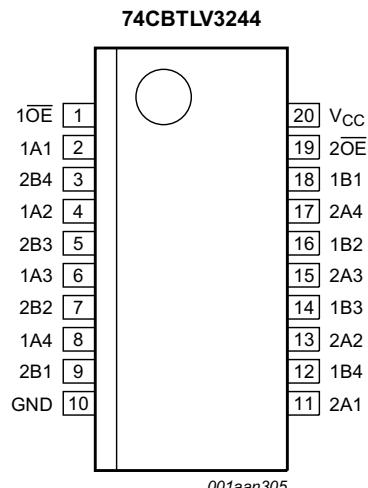
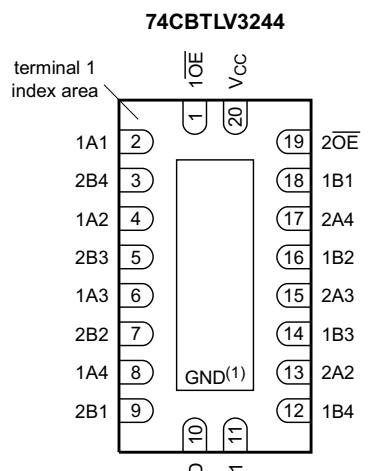


Fig 4. Pin configuration for SSOP20 (SOT724-1)



Transparent top view

- (1) This is not a supply pin. The substrate is attached to this pad using conductive die attach material. There is no electrical or mechanical requirement to solder this pad. However, if it is soldered, the solder land should remain floating or be connected to GND.

Fig 5. Pin configuration for DHVQFN20 (SOT764-1)

## 5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
$\overline{1OE}$ , $\overline{2OE}$	1, 19	output enable input (active LOW)
1A1 to 1A4	2, 4, 6, 8	data input/output (A port)
2B1 to 2B4	9, 7, 5, 3	data input/output (A port)
GND	10	ground (0 V)
2A1 to 2A4	11, 13, 15, 17	data input/output (B port)
1B1 to 1B4	18, 16, 14, 12	data input/output (B port)
$V_{CC}$	20	positive supply voltage

## 6. Functional description

Table 3. Function selection<sup>[1]</sup>

Input	Input/output
$nOE$	$nAn$ , $nBn$
L	$nAn = nBn$
H	Z

[1] H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF-state.

## 7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		-0.5	+4.6	V
$V_I$	input voltage	<sup>[1]</sup>	-0.5	+4.6	V
$V_{SW}$	switch voltage	enable and disable mode	<sup>[1]</sup>	-0.5	$V_{CC} + 0.5$
$I_{IK}$	input clamping current	$V_I < -0.5$ V	-50	-	mA
$I_{SK}$	switch clamping current	$V_I < -0.5$ V	-50	-	mA
$I_{SW}$	switch current	$V_{SW} = 0$ V to $V_{CC}$	-	$\pm 128$	mA
$I_{CC}$	supply current		-	+100	mA
$I_{GND}$	ground current		-100	-	mA
$T_{stg}$	storage temperature		-65	+150	°C
$P_{tot}$	total power dissipation	$T_{amb} = -40$ °C to +125 °C	<sup>[2]</sup>	-	500 mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SSOP20 and TSSOP20 packages: above 60 °C the value of  $P_{tot}$  derates linearly at 5.5 mW/K.

For DHVQFN20 packages: above 60 °C the value of  $P_{tot}$  derates linearly at 4.5 mW/K.

## 8. Recommended operating conditions

**Table 5. Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		2.3	3.6	V
$V_I$	input voltage		0	3.6	V
$V_{SW}$	switch voltage	enable and disable mode	0	$V_{CC}$	V
$T_{amb}$	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 2.3 \text{ V to } 3.6 \text{ V}$	[1]	-	200 ns/V

[1] Applies to control signal levels.

## 9. Static characteristics

**Table 6. Static characteristics**

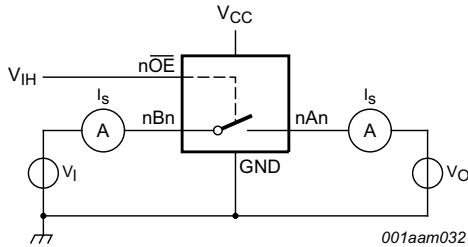
At recommended operating conditions voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	$T_{amb} = -40 \text{ °C to } +85 \text{ °C}$			$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$			Unit
			Min	Typ[1]	Max	Min	Max		
$V_{IH}$	HIGH-level input voltage	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.7	-	-	1.7	-	V	
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	2.0	-	-	2.0	-	V	
$V_{IL}$	LOW-level input voltage	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	-	0.7	V	
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	-	0.9	-	0.9	V	
$I_I$	input leakage current	pin $n\overline{OE}$ ; $V_I = \text{GND to } V_{CC}$ ; $V_{CC} = 3.6 \text{ V}$	-	-	$\pm 1$	-	$\pm 20$	$\mu\text{A}$	
$I_{S(OFF)}$	OFF-state leakage current	$V_{CC} = 3.6 \text{ V}$ ; see <a href="#">Figure 6</a>	-	-	$\pm 1$	-	$\pm 20$	$\mu\text{A}$	
$I_{S(ON)}$	ON-state leakage current	$V_{CC} = 3.6 \text{ V}$ ; see <a href="#">Figure 7</a>	-	-	$\pm 1$	-	$\pm 20$	$\mu\text{A}$	
$I_{OFF}$	power-off leakage current	$V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}$ ; $V_{CC} = 0 \text{ V}$	-	-	$\pm 10$	-	$\pm 50$	$\mu\text{A}$	
$I_{CC}$	supply current	$V_I = \text{GND or } V_{CC}$ ; $I_O = 0 \text{ A}$ ; $V_{SW} = \text{GND or } V_{CC}$ ; $V_{CC} = 3.6 \text{ V}$	-	-	10	-	50	$\mu\text{A}$	
$\Delta I_{CC}$	additional supply current	pin $n\overline{OE}$ ; $V_I = V_{CC} - 0.6 \text{ V}$ ; <a href="#">[2]</a> $V_{SW} = \text{GND or } V_{CC}$ ; $V_{CC} = 3.6 \text{ V}$	-	-	300	-	2000	$\mu\text{A}$	
$C_I$	input capacitance	pin $n\overline{OE}$ ; $V_{CC} = 3.3 \text{ V}$ ; $V_I = 0 \text{ V to } 3.3 \text{ V}$	-	0.9	-	-	-	$\text{pF}$	
$C_{S(OFF)}$	OFF-state capacitance	$V_{CC} = 3.3 \text{ V}$ ; $V_I = 0 \text{ V to } 3.3 \text{ V}$	-	5.2	-	-	-	$\text{pF}$	
$C_{S(ON)}$	ON-state capacitance	$V_{CC} = 3.3 \text{ V}$ ; $V_I = 0 \text{ V to } 3.3 \text{ V}$	-	14.3	-	-	-	$\text{pF}$	

[1] All typical values are measured at  $T_{amb} = 25 \text{ °C}$ .

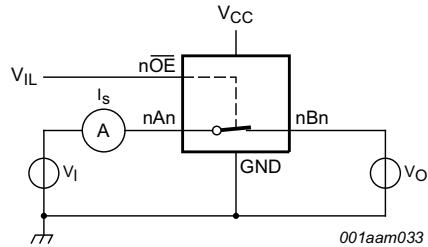
[2] One input at 3 V, other inputs at  $V_{CC}$  or GND.

## 9.1 Test circuits



$V_I = V_{CC}$  or GND and  $V_O = GND$  or  $V_{CC}$ .

**Fig 6. Test circuit for measuring OFF-state leakage current (one switch)**



$V_I = V_{CC}$  or GND and  $V_O = \text{open circuit}$ .

**Fig 7. Test circuit for measuring ON-state leakage current (one switch)**

## 9.2 ON resistance

**Table 7. Resistance  $R_{ON}$**

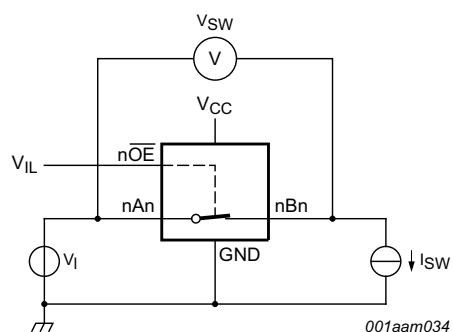
At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 8](#).

Symbol	Parameter	Conditions	$T_{amb} = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$			$T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$			Unit
			Min	Typ <sup>[1]</sup>	Max	Min	Max		
$R_{ON}$	ON resistance	$V_{CC} = 2.3\text{ V}$ to $2.7\text{ V}$ ; <a href="#">[2]</a> see <a href="#">Figure 9</a> to <a href="#">Figure 11</a>							
		$I_{SW} = 64\text{ mA}$ ; $V_I = 0\text{ V}$	-	4.2	8.0	-	15.0	$\Omega$	
		$I_{SW} = 24\text{ mA}$ ; $V_I = 0\text{ V}$	-	4.2	8.0	-	15.0	$\Omega$	
		$I_{SW} = 15\text{ mA}$ ; $V_I = 1.7\text{ V}$	-	8.4	40	-	60.0	$\Omega$	
		$V_{CC} = 3.0\text{ V}$ to $3.6\text{ V}$ ; see <a href="#">Figure 12</a> to <a href="#">Figure 14</a>							
		$I_{SW} = 64\text{ mA}$ ; $V_I = 0\text{ V}$	-	4.0	7.0	-	11.0	$\Omega$	
		$I_{SW} = 24\text{ mA}$ ; $V_I = 0\text{ V}$	-	4.0	7.0	-	11.0	$\Omega$	
		$I_{SW} = 15\text{ mA}$ ; $V_I = 2.4\text{ V}$	-	6.2	15	-	25.5	$\Omega$	

[1] Typical values are measured at  $T_{amb} = 25^{\circ}\text{C}$  and nominal  $V_{CC}$ .

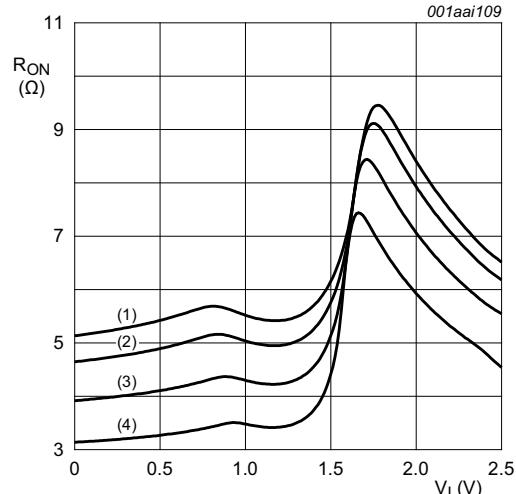
[2] Measured by the voltage drop between the A and B terminals at the indicated current through the switch. ON-state resistance is determined by the lower of the voltages of the two (A or B) terminals.

### 9.3 ON resistance test circuit and graphs



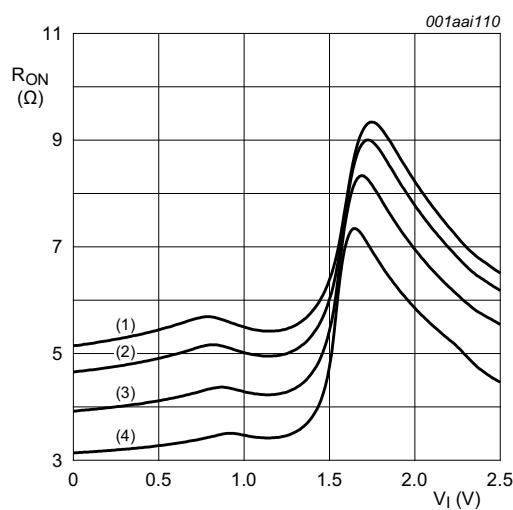
$$R_{ON} = V_{SW} / I_{SW}$$

**Fig 8. Test circuit for measuring ON resistance (one switch)**



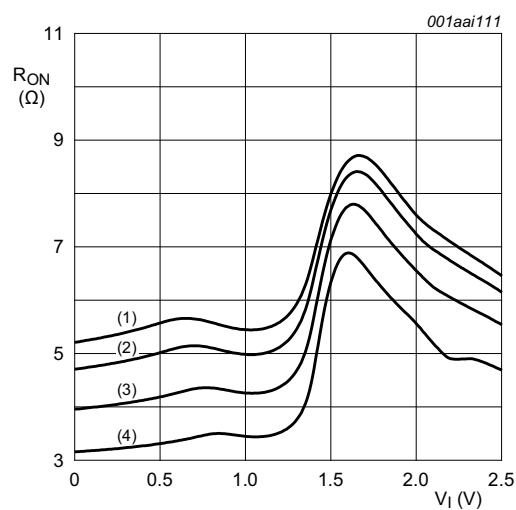
- (1)  $T_{amb} = 125 \text{ }^{\circ}\text{C}$ .
- (2)  $T_{amb} = 85 \text{ }^{\circ}\text{C}$ .
- (3)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$ .
- (4)  $T_{amb} = -40 \text{ }^{\circ}\text{C}$ .

**Fig 9. ON resistance as a function of input voltage;  $V_{CC} = 2.5 \text{ V}$ ;  $I_{SW} = 15 \text{ mA}$**



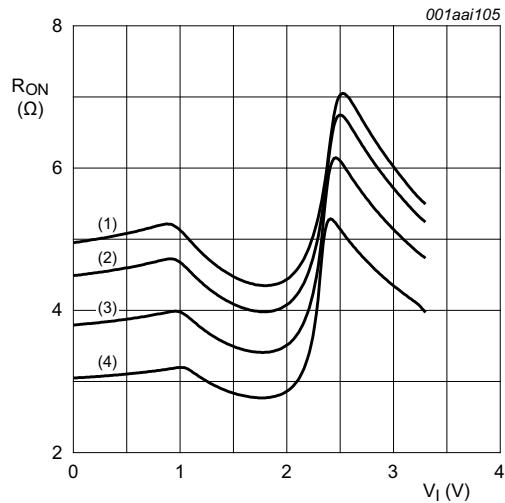
- (1)  $T_{amb} = 125 \text{ }^{\circ}\text{C}$ .
- (2)  $T_{amb} = 85 \text{ }^{\circ}\text{C}$ .
- (3)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$ .
- (4)  $T_{amb} = -40 \text{ }^{\circ}\text{C}$ .

**Fig 10. ON resistance as a function of input voltage;  $V_{CC} = 2.5 \text{ V}$ ;  $I_{SW} = 24 \text{ mA}$**



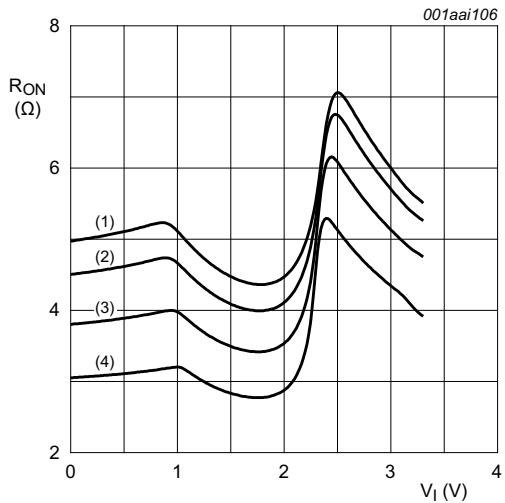
- (1)  $T_{amb} = 125 \text{ }^{\circ}\text{C}$ .
- (2)  $T_{amb} = 85 \text{ }^{\circ}\text{C}$ .
- (3)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$ .
- (4)  $T_{amb} = -40 \text{ }^{\circ}\text{C}$ .

**Fig 11. ON resistance as a function of input voltage;  $V_{CC} = 2.5 \text{ V}$ ;  $I_{SW} = 64 \text{ mA}$**



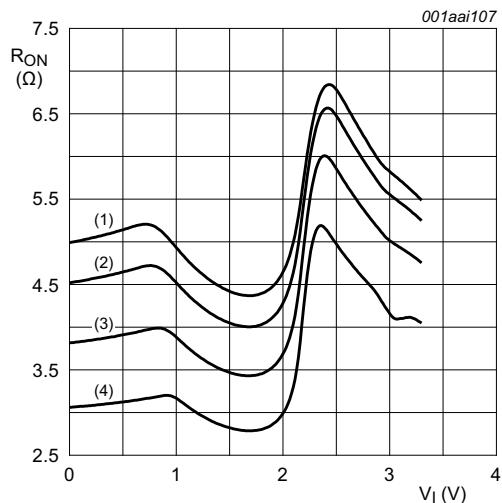
- (1)  $T_{amb} = 125$  °C.
- (2)  $T_{amb} = 85$  °C.
- (3)  $T_{amb} = 25$  °C.
- (4)  $T_{amb} = -40$  °C.

**Fig 12. ON resistance as a function of input voltage;  $V_{CC} = 3.3$  V;  $I_{SW} = 15$  mA**



- (1)  $T_{amb} = 125$  °C.
- (2)  $T_{amb} = 85$  °C.
- (3)  $T_{amb} = 25$  °C.
- (4)  $T_{amb} = -40$  °C.

**Fig 13. ON resistance as a function of input voltage;  $V_{CC} = 3.3$  V;  $I_{SW} = 24$  mA**



- (1)  $T_{amb} = 125$  °C.
- (2)  $T_{amb} = 85$  °C.
- (3)  $T_{amb} = 25$  °C.
- (4)  $T_{amb} = -40$  °C.

**Fig 14. ON resistance as a function of input voltage;  $V_{CC} = 3.3$  V;  $I_{SW} = 64$  mA**

## 10. Dynamic characteristics

**Table 8. Dynamic characteristics**

GND = 0 V; for test circuit see [Figure 17](#)

Symbol	Parameter	Conditions	$T_{amb} = -40^{\circ}C$ to $+85^{\circ}C$		$T_{amb} = -40^{\circ}C$ to $+125^{\circ}C$		Unit
			Min	Typ <sup>[1]</sup>	Max	Min	
$t_{pd}$	propagation delay	nAn to nBn or nBn to nAn; see <a href="#">Figure 15</a> <sup>[2][3]</sup>					
		$V_{CC} = 2.3\text{ V}$ to $2.7\text{ V}$	-	-	0.13	-	0.20 ns
		$V_{CC} = 3.0\text{ V}$ to $3.6\text{ V}$	-	-	0.20	-	0.31 ns
$t_{en}$	enable time	nOE to nAn or nBn; see <a href="#">Figure 16</a> <sup>[4]</sup>					
		$V_{CC} = 2.3\text{ V}$ to $2.7\text{ V}$	1.0	3.0	5.0	1.0	7.0 ns
		$V_{CC} = 3.0\text{ V}$ to $3.6\text{ V}$	1.0	2.6	4.3	1.0	6.0 ns
$t_{dis}$	disable time	nOE to nAn or nBn; see <a href="#">Figure 16</a> <sup>[5]</sup>					
		$V_{CC} = 2.3\text{ V}$ to $2.7\text{ V}$	1.0	2.6	5.5	1.0	7.5 ns
		$V_{CC} = 3.0\text{ V}$ to $3.6\text{ V}$	1.0	3.2	5.5	1.0	7.5 ns

[1] All typical values are measured at  $T_{amb} = 25^{\circ}C$  and at nominal  $V_{CC}$ .

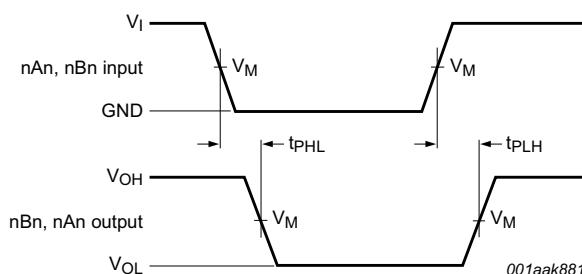
[2] The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the load capacitance, when driven by an ideal voltage source (zero output impedance).

[3]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

[4]  $t_{en}$  is the same as  $t_{PZH}$  and  $t_{PZL}$ .

[5]  $t_{dis}$  is the same as  $t_{PHZ}$  and  $t_{PLZ}$ .

## 11. Waveforms



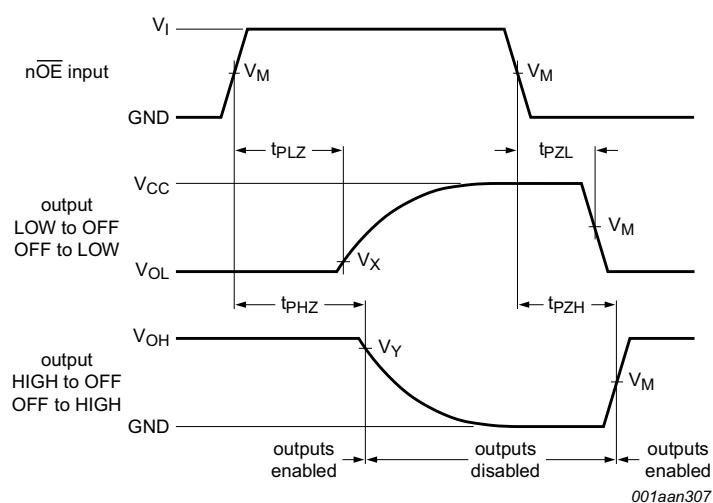
Measurement points are given in [Table 9](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig 15. The data input (nAn, nBn) to output (nBn, nAn) propagation delay times**

**Table 9. Measurement points**

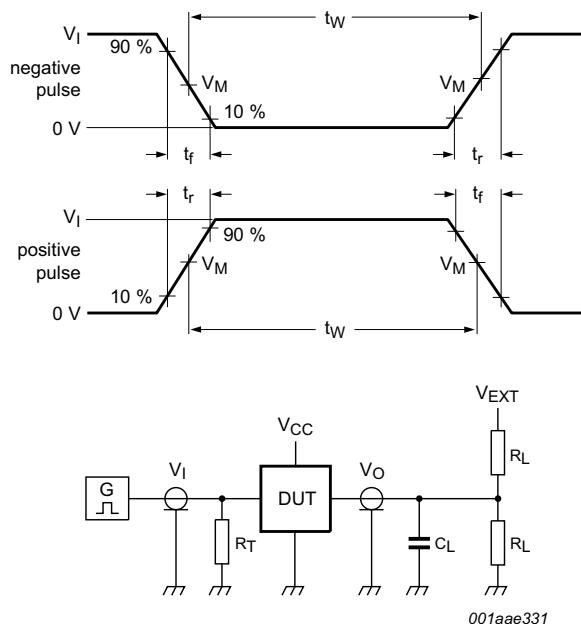
Supply voltage	Input	Output				
$V_{CC}$	$V_M$	$V_I$	$t_r = t_f$	$V_M$	$V_X$	$V_Y$
2.3 V to 2.7 V	0.5 $V_{CC}$	$V_{CC}$	$\leq 2.0\text{ ns}$	0.5 $V_{CC}$	$V_{OL} + 0.15\text{ V}$	$V_{OH} - 0.15\text{ V}$
3.0 V to 3.6 V	0.5 $V_{CC}$	$V_{CC}$	$\leq 2.0\text{ ns}$	0.5 $V_{CC}$	$V_{OL} + 0.3\text{ V}$	$V_{OH} - 0.3\text{ V}$



Measurement points are given in [Table 9](#).

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

**Fig 16. Enable and disable times**



Test data is given in [Table 10](#).

Definitions for test circuit:

$R_L$  = Load resistance.

$C_L$  = Load capacitance including jig and probe capacitance.

$R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

$V_{EXT}$  = External voltage for measuring switching times.

**Fig 17. Test circuit for measuring switching times**

**Table 10. Test data**

Supply voltage	Load		$V_{EXT}$		
$V_{CC}$	$C_L$	$R_L$	$t_{PLH}, t_{PHL}$	$t_{PZH}, t_{PHZ}$	$t_{PZL}, t_{PLZ}$
2.3 V to 2.7 V	30 pF	500 $\Omega$	open	GND	2 $V_{CC}$
3.0 V to 3.6 V	50 pF	500 $\Omega$	open	GND	2 $V_{CC}$

## 11.1 Additional dynamic characteristics

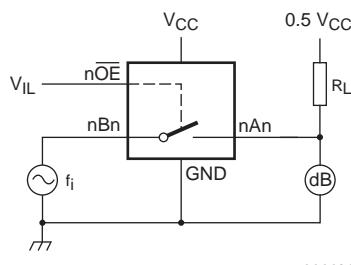
**Table 11. Additional dynamic characteristics**

At recommended operating conditions; voltages are referenced to GND (ground = 0 V);  $V_I$  = GND or  $V_{CC}$  (unless otherwise specified);  $t_f = t_{f_i} \leq 2.5$  ns.

Symbol	Parameter	Conditions	$T_{amb} = 25$ °C			Unit
			Min	Typ	Max	
$f_{(-3dB)}$	–3 dB frequency response	$V_{CC} = 3.3$ V; $R_L = 50$ Ω; see Figure 18 [1]	-	406	-	MHz

[1]  $f_i$  is biased at 0.5V<sub>CC</sub>.

## 11.2 Test circuits



aaa-0000028

$n\overline{OE}$  connected to GND; Adjust  $f_i$  voltage to obtain 0 dBm level at output. Increase  $f_i$  frequency until dB meter reads –3 dB.

**Fig 18. Test circuit for measuring the frequency response when channel is in ON-state**

## 12. Package outline

SSOP20: plastic shrink small outline package; 20 leads; body width 3.9 mm; lead pitch 0.635 mm SOT724-1

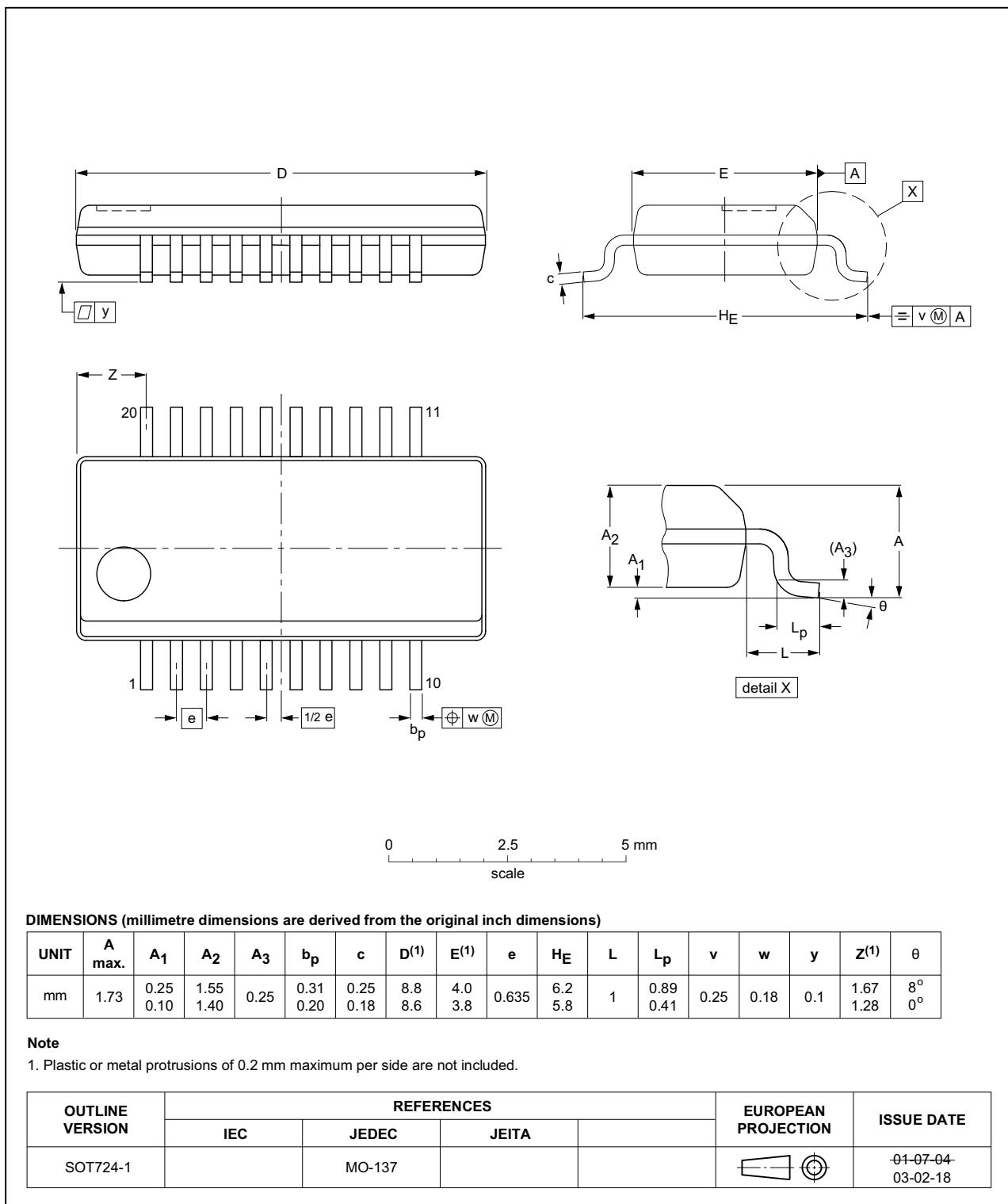


Fig 19. Package outline SOT724-1 (SSOP20)

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1

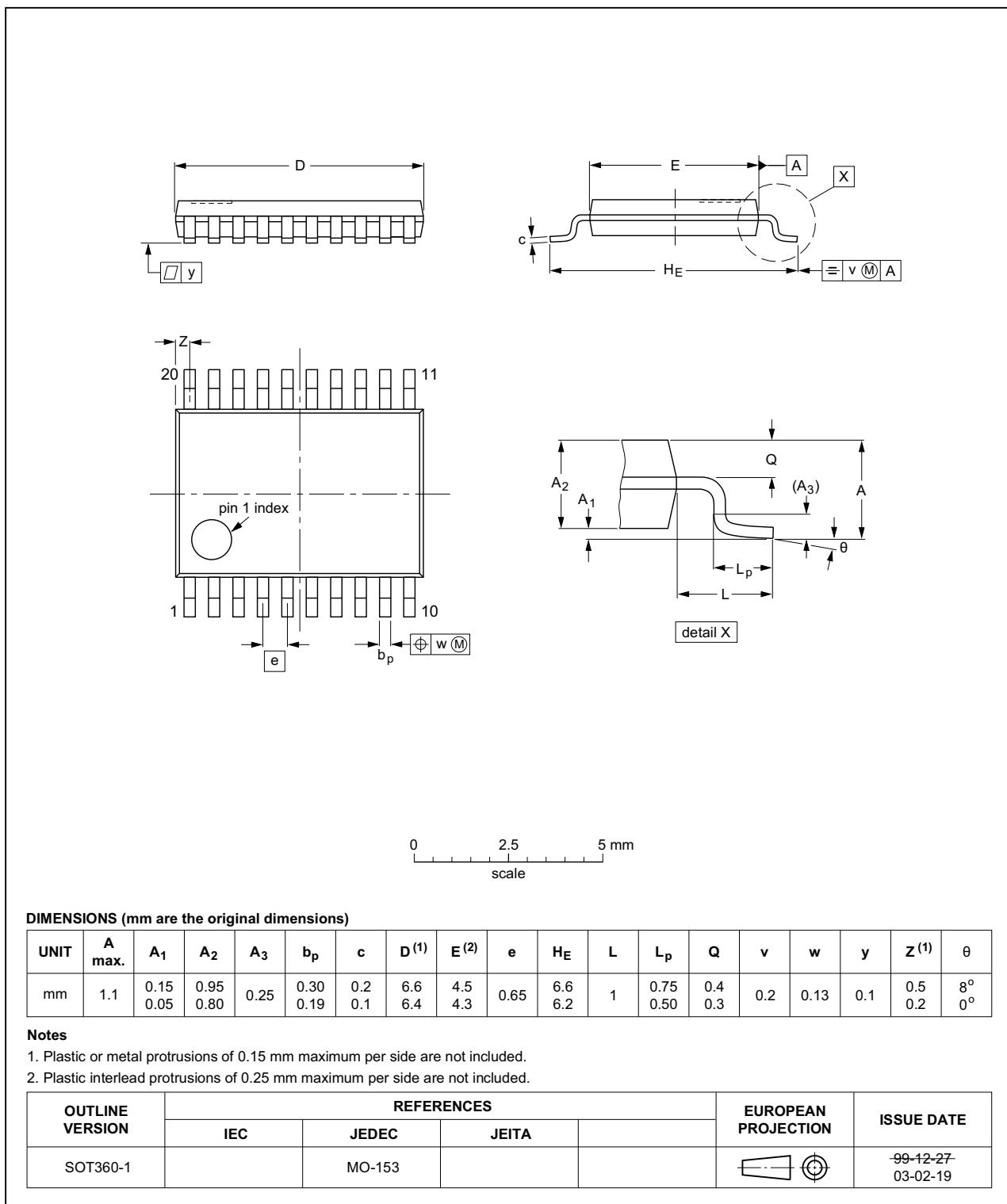


Fig 20. Package outline SOT360-1 (TSSOP20)

**DHVQFN20: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 20 terminals; body 2.5 x 4.5 x 0.85 mm**

SOT764-1

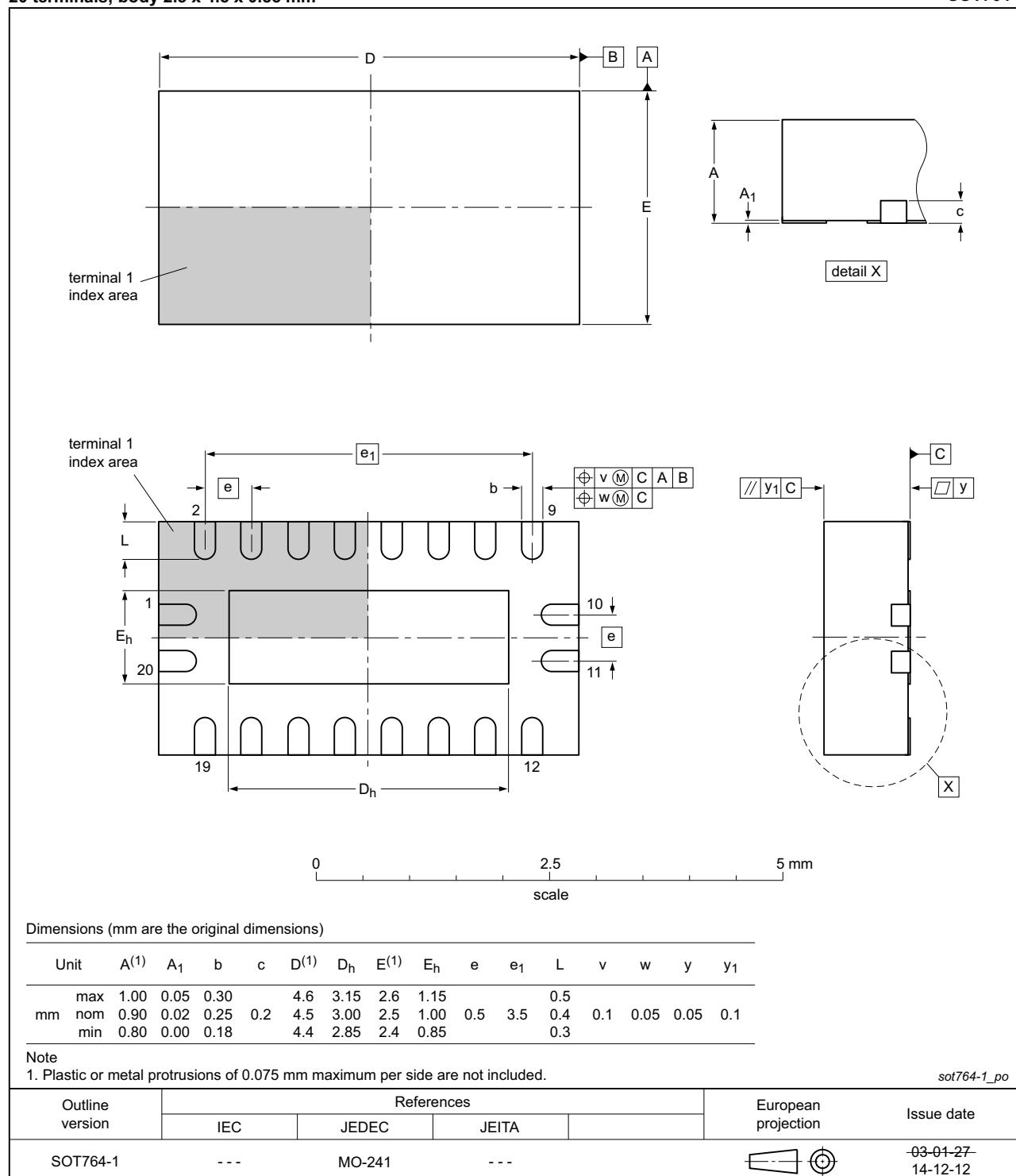


Fig 21. Package outline SOT764-1 (DHVQFN20)

## 13. Abbreviations

**Table 12. Abbreviations**

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model

## 14. Revision history

**Table 13. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
74CBTLV3244 v.3	20161108	Product data sheet	-	74CBTLV3244 v.2
Modifications:	• <a href="#">Section 11.1</a> and <a href="#">Section 11.2</a> added.			
74CBTLV3244 v.2	20111215	Product data sheet	-	74CBTLV3244 v.1
Modifications:	• Legal pages updated.			
74CBTLV3244 v.1	20101228	Product data sheet	-	-

## 15. Legal information

### 15.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

### 15.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

**Suitability for use** — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

### 15.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

## 15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 16. Contact information

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: [salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)

## 17. Contents

---

<b>1</b>	<b>General description</b>	<b>1</b>
<b>2</b>	<b>Features and benefits</b>	<b>1</b>
<b>3</b>	<b>Ordering information</b>	<b>2</b>
<b>4</b>	<b>Functional diagram</b>	<b>2</b>
<b>5</b>	<b>Pinning information</b>	<b>3</b>
5.1	Pinning	3
5.2	Pin description	4
<b>6</b>	<b>Functional description</b>	<b>4</b>
<b>7</b>	<b>Limiting values</b>	<b>4</b>
<b>8</b>	<b>Recommended operating conditions</b>	<b>5</b>
<b>9</b>	<b>Static characteristics</b>	<b>5</b>
9.1	Test circuits	6
9.2	ON resistance	6
9.3	ON resistance test circuit and graphs	7
<b>10</b>	<b>Dynamic characteristics</b>	<b>9</b>
<b>11</b>	<b>Waveforms</b>	<b>9</b>
11.1	Additional dynamic characteristics	12
11.2	Test circuits	12
<b>12</b>	<b>Package outline</b>	<b>13</b>
<b>13</b>	<b>Abbreviations</b>	<b>16</b>
<b>14</b>	<b>Revision history</b>	<b>16</b>
<b>15</b>	<b>Legal information</b>	<b>17</b>
15.1	Data sheet status	17
15.2	Definitions	17
15.3	Disclaimers	17
15.4	Trademarks	18
<b>16</b>	<b>Contact information</b>	<b>18</b>
<b>17</b>	<b>Contents</b>	<b>19</b>