

74AUP1G374-Q100

Low-power D-type flip-flop; positive-edge trigger; 3-state

Rev. 1 — 19 February 2013

Product data sheet

1. General description

The 74AUP1G374-Q100 provides the single D-type flip-flop with 3-state output. The flip-flop stores the state of data input (D) that meets the set-up and hold times requirements on the LOW-to-HIGH CP transition. When pin \overline{OE} is LOW, the contents of the flip-flop is available at the (Q) output. When pin \overline{OE} is HIGH, the output goes to the high-impedance OFF-state. Operation of input pin \overline{OE} does not affect the state of the flip-flop.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - ◆ Specified from -40°C to $+85^{\circ}\text{C}$ and from -40°C to $+125^{\circ}\text{C}$
- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - ◆ JESD8-12 (0.8 V to 1.3 V)
 - ◆ JESD8-11 (0.9 V to 1.65 V)
 - ◆ JESD8-7 (1.2 V to 1.95 V)
 - ◆ JESD8-5 (1.8 V to 2.7 V)
 - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - ◆ MIL-STD-883, method 3015 Class 3A. Exceeds 5000 V
 - ◆ HBM JESD22-A114F Class 3A. Exceeds 5000 V
 - ◆ MM JESD22-A115-A exceeds 200 V ($C = 200 \text{ pF}$, $R = 0 \Omega$)
- Low static power consumption; $I_{CC} = 0.9 \mu\text{A}$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot $< 10\%$ of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation

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3. Ordering information

Table 1. Ordering information

| Type number | Package | Temperature range | Name | Description | Version |
|-------------------|---------|-------------------|------|--|---------|
| 74AUP1G374GW-Q100 | SC-88 | −40 °C to +125 °C | | plastic surface-mounted package; 6 leads | SOT363 |

4. Marking

Table 2. Marking

| Type number | Marking code ^[1] |
|-------------------|-----------------------------|
| 74AUP1G374GW-Q100 | aX |

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

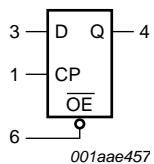


Fig 1. Logic symbol

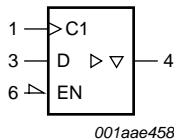


Fig 2. IEC logic symbol

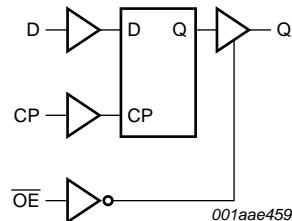


Fig 3. Logic diagram

6. Pinning information

6.1 Pinning

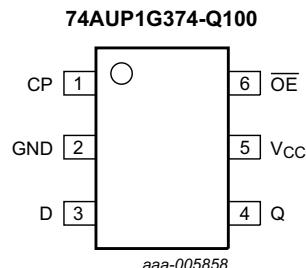


Fig 4. Pin configuration SOT363

6.2 Pin description

Table 3. Pin description

| Symbol | Pin | Description |
|-----------------|-----|---|
| CP | 1 | clock input (LOW-to-HIGH, edge-triggered) |
| GND | 2 | ground (0 V) |
| D | 3 | data input |
| Q | 4 | 3-state flip-flop output |
| V _{CC} | 5 | supply voltage |
| \overline{OE} | 6 | output enable input (active LOW) |

7. Functional description

Table 4. Function table^[1]

| Operating mode | Input | | | Internal flip-flop | Output |
|----------------------------------|-------|----|---|--------------------|--------|
| | OE | CP | D | | |
| Load and read register | L | ↑ | I | L | L |
| | L | ↑ | h | H | H |
| Load register and disable output | H | ↑ | I | L | Z |
| | H | ↑ | h | H | Z |

[1] H = HIGH voltage level;

h = HIGH voltage level one set-up time prior to the HIGH-to-LOW LE transition;

L = LOW voltage level;

I = LOW voltage level one set-up time prior to the HIGH-to-LOW LE transition;

Z = high-impedance OFF-state;

↑ = LOW-to-HIGH clock transition.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|-------------------------|---|----------|------|------|
| V _{CC} | supply voltage | | -0.5 | +4.6 | V |
| I _{IK} | input clamping current | V _I < 0 V | -50 | - | mA |
| V _I | input voltage | | [1] -0.5 | +4.6 | V |
| I _{OK} | output clamping current | V _O < 0 V | -50 | - | mA |
| V _O | output voltage | Active mode and Power-down mode | [1] -0.5 | +4.6 | V |
| I _O | output current | V _O = 0 V to V _{CC} | - | ±20 | mA |
| I _{CC} | supply current | | - | 50 | mA |
| I _{GND} | ground current | | -50 | - | mA |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| P _{tot} | total power dissipation | T _{amb} = -40 °C to +125 °C | [2] - | 250 | mW |

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SC-88 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

| Symbol | Parameter | Conditions | Min | Max | Unit |
|---------------------|-------------------------------------|---------------------------------|-----|----------|------|
| V_{CC} | supply voltage | | 0.8 | 3.6 | V |
| V_I | input voltage | | 0 | 3.6 | V |
| V_O | output voltage | Active mode | 0 | V_{CC} | V |
| | | Power-down mode; $V_{CC} = 0$ V | 0 | 3.6 | V |
| T_{amb} | ambient temperature | | -40 | +125 | °C |
| $\Delta t/\Delta V$ | input transition rise and fall rate | $V_{CC} = 0.8$ V to 3.6 V | 0 | 200 | ns/V |

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-------------------------------------|---------------------------|---|----------------------|-----|----------------------|------|
| $T_{amb} = 25$ °C | | | | | | |
| V_{IH} | HIGH-level input voltage | $V_{CC} = 0.8$ V | $0.70 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 0.9$ V to 1.95 V | $0.65 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 2.3$ V to 2.7 V | 1.6 | - | - | V |
| | | $V_{CC} = 3.0$ V to 3.6 V | 2.0 | - | - | V |
| V_{IL} | LOW-level input voltage | $V_{CC} = 0.8$ V | - | - | $0.30 \times V_{CC}$ | V |
| | | $V_{CC} = 0.9$ V to 1.95 V | - | - | $0.35 \times V_{CC}$ | V |
| | | $V_{CC} = 2.3$ V to 2.7 V | - | - | 0.7 | V |
| | | $V_{CC} = 3.0$ V to 3.6 V | - | - | 0.9 | V |
| V_{OH} | HIGH-level output voltage | $V_I = V_{IH}$ or V_{IL} | | | | |
| | | $I_O = -20 \mu A$; $V_{CC} = 0.8$ V to 3.6 V | $V_{CC} - 0.1$ | - | - | V |
| | | $I_O = -1.1$ mA; $V_{CC} = 1.1$ V | $0.75 \times V_{CC}$ | - | - | V |
| | | $I_O = -1.7$ mA; $V_{CC} = 1.4$ V | 1.11 | - | - | V |
| | | $I_O = -1.9$ mA; $V_{CC} = 1.65$ V | 1.32 | - | - | V |
| | | $I_O = -2.3$ mA; $V_{CC} = 2.3$ V | 2.05 | - | - | V |
| | | $I_O = -3.1$ mA; $V_{CC} = 2.3$ V | 1.9 | - | - | V |
| | | $I_O = -2.7$ mA; $V_{CC} = 3.0$ V | 2.72 | - | - | V |
| | | $I_O = -4.0$ mA; $V_{CC} = 3.0$ V | 2.6 | - | - | V |
| V_{OL} | LOW-level output voltage | $V_I = V_{IH}$ or V_{IL} | | | | |
| | | $I_O = 20 \mu A$; $V_{CC} = 0.8$ V to 3.6 V | - | - | 0.1 | V |
| | | $I_O = 1.1$ mA; $V_{CC} = 1.1$ V | - | - | $0.3 \times V_{CC}$ | V |
| | | $I_O = 1.7$ mA; $V_{CC} = 1.4$ V | - | - | 0.31 | V |
| | | $I_O = 1.9$ mA; $V_{CC} = 1.65$ V | - | - | 0.31 | V |
| | | $I_O = 2.3$ mA; $V_{CC} = 2.3$ V | - | - | 0.31 | V |
| | | $I_O = 3.1$ mA; $V_{CC} = 2.3$ V | - | - | 0.44 | V |
| | | $I_O = 2.7$ mA; $V_{CC} = 3.0$ V | - | - | 0.31 | V |
| | | $I_O = 4.0$ mA; $V_{CC} = 3.0$ V | - | - | 0.44 | V |

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|------------------|--------------------------------------|--|-----|-----|-----------|---------------|
| I_I | input leakage current | $V_I = \text{GND to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$ | - | - | ± 0.1 | μA |
| I_{OZ} | OFF-state output current | $V_I = V_{IH} \text{ or } V_{IL}; V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$ | - | - | ± 0.1 | μA |
| I_{OFF} | power-off leakage current | $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$ | - | - | ± 0.2 | μA |
| ΔI_{OFF} | additional power-off leakage current | $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 0.2 \text{ V}$ | - | - | ± 0.2 | μA |
| I_{CC} | supply current | $V_I = \text{GND or } V_{CC}; I_O = 0 \text{ A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$ | - | - | 0.5 | μA |
| ΔI_{CC} | additional supply current | $V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ | [1] | - | 40 | μA |
| C_I | input capacitance | $V_{CC} = 0 \text{ V to } 3.6 \text{ V}; V_I = \text{GND or } V_{CC}$ | - | 0.8 | - | pF |
| C_O | output capacitance | output enabled; $V_O = \text{GND}; V_{CC} = 0 \text{ V}$ | - | 1.7 | - | pF |
| | | output disabled; $V_{CC} = 0 \text{ V to } 3.6 \text{ V}; V_O = \text{GND or } V_{CC}$ | - | 1.5 | - | pF |

 $T_{amb} = -40 \text{ }^{\circ}\text{C to } +85 \text{ }^{\circ}\text{C}$

| | | | | | | |
|----------|---------------------------|---|----------------------|---|----------------------|---|
| V_{IH} | HIGH-level input voltage | $V_{CC} = 0.8 \text{ V}$ | $0.70 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 0.9 \text{ V to } 1.95 \text{ V}$ | $0.65 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 1.6 | - | - | V |
| | | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$ | 2.0 | - | - | V |
| V_{IL} | LOW-level input voltage | $V_{CC} = 0.8 \text{ V}$ | - | - | $0.30 \times V_{CC}$ | V |
| | | $V_{CC} = 0.9 \text{ V to } 1.95 \text{ V}$ | - | - | $0.35 \times V_{CC}$ | V |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | - | - | 0.7 | V |
| | | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$ | - | - | 0.9 | V |
| V_{OH} | HIGH-level output voltage | $V_I = V_{IH} \text{ or } V_{IL}$ | | | | |
| | | $I_O = -20 \mu\text{A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$ | $V_{CC} - 0.1$ | - | - | V |
| | | $I_O = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$ | $0.7 \times V_{CC}$ | - | - | V |
| | | $I_O = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$ | 1.03 | - | - | V |
| | | $I_O = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$ | 1.30 | - | - | V |
| | | $I_O = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | 1.97 | - | - | V |
| | | $I_O = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | 1.85 | - | - | V |
| | | $I_O = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | 2.67 | - | - | V |
| V_{OL} | LOW-level output voltage | $I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | 2.55 | - | - | V |
| | | $V_I = V_{IH} \text{ or } V_{IL}$ | | | | |
| | | $I_O = 20 \mu\text{A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$ | - | - | 0.1 | V |
| | | $I_O = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$ | - | - | $0.3 \times V_{CC}$ | V |
| | | $I_O = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$ | - | - | 0.37 | V |
| | | $I_O = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$ | - | - | 0.35 | V |
| | | $I_O = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | - | - | 0.33 | V |
| | | $I_O = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | - | - | 0.45 | V |
| | | $I_O = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | - | - | 0.33 | V |
| | | $I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | - | - | 0.45 | V |

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|------------------|--------------------------------------|--|-----|-----|-----------|---------------|
| I_I | input leakage current | $V_I = \text{GND to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$ | - | - | ± 0.5 | μA |
| I_{OZ} | OFF-state output current | $V_I = V_{IH} \text{ or } V_{IL}; V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$ | - | - | ± 0.5 | μA |
| I_{OFF} | power-off leakage current | $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$ | - | - | ± 0.5 | μA |
| ΔI_{OFF} | additional power-off leakage current | $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 0.2 \text{ V}$ | - | - | ± 0.6 | μA |
| I_{CC} | supply current | $V_I = \text{GND or } V_{CC}; I_O = 0 \text{ A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$ | - | - | 0.9 | μA |
| ΔI_{CC} | additional supply current | $V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$ | [1] | - | 50 | μA |

 $T_{amb} = -40 \text{ }^{\circ}\text{C to } +125 \text{ }^{\circ}\text{C}$

| | | | | | | |
|-----------|---------------------------|--|----------------------|---|----------------------|---------------|
| V_{IH} | HIGH-level input voltage | $V_{CC} = 0.8 \text{ V}$ | $0.75 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 0.9 \text{ V to } 1.95 \text{ V}$ | $0.70 \times V_{CC}$ | - | - | V |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 1.6 | - | - | V |
| | | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$ | 2.0 | - | - | V |
| V_{IL} | LOW-level input voltage | $V_{CC} = 0.8 \text{ V}$ | - | - | $0.25 \times V_{CC}$ | V |
| | | $V_{CC} = 0.9 \text{ V to } 1.95 \text{ V}$ | - | - | $0.30 \times V_{CC}$ | V |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | - | - | 0.7 | V |
| | | $V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$ | - | - | 0.9 | V |
| V_{OH} | HIGH-level output voltage | $V_I = V_{IH} \text{ or } V_{IL}$ | | | | |
| | | $I_O = -20 \mu\text{A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$ | $V_{CC} - 0.11$ | - | - | V |
| | | $I_O = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$ | $0.6 \times V_{CC}$ | - | - | V |
| | | $I_O = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$ | 0.93 | - | - | V |
| | | $I_O = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$ | 1.17 | - | - | V |
| | | $I_O = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | 1.77 | - | - | V |
| | | $I_O = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | 1.67 | - | - | V |
| | | $I_O = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | 2.40 | - | - | V |
| V_{OL} | LOW-level output voltage | $I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | 2.30 | - | - | V |
| | | $V_I = V_{IH} \text{ or } V_{IL}$ | | | | |
| | | $I_O = 20 \mu\text{A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$ | - | - | 0.11 | V |
| | | $I_O = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$ | - | - | $0.33 \times V_{CC}$ | V |
| | | $I_O = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$ | - | - | 0.41 | V |
| | | $I_O = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$ | - | - | 0.39 | V |
| | | $I_O = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | - | - | 0.36 | V |
| | | $I_O = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$ | - | - | 0.50 | V |
| I_I | input leakage current | $I_O = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | - | - | 0.36 | V |
| | | $I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$ | - | - | 0.50 | V |
| I_{OZ} | OFF-state output current | $V_I = V_{IH} \text{ or } V_{IL}; V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$ | - | - | ± 0.75 | μA |
| | | $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$ | - | - | ± 0.75 | μA |
| I_{OFF} | power-off leakage current | $V_I \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$ | - | - | ± 0.75 | μA |

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|------------------|--------------------------------------|---|-----|-----|------------|---------|
| ΔI_{OFF} | additional power-off leakage current | V_I or V_O = 0 V to 3.6 V; V_{CC} = 0 V to 0.2 V | - | - | ± 0.75 | μA |
| I_{CC} | supply current | V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V | - | - | 1.4 | μA |
| ΔI_{CC} | additional supply current | V_I = V_{CC} – 0.6 V; I_O = 0 A; V_{CC} = 3.3 V | [1] | - | 75 | μA |

[1] One input at V_{CC} – 0.6 V, other input at V_{CC} or GND.

11. Dynamic characteristics

Table 8. Dynamic characteristicsVoltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

| Symbol | Parameter | Conditions | 25 °C | | | –40 °C to +125 °C | | | Unit |
|--------------------------------|-------------------|---------------------------------------|-------|--------|------|-------------------|-------------|--------------|------|
| | | | Min | Typ[1] | Max | Min (85 °C) | Max (85 °C) | Min (125 °C) | |
| C_L = 5 pF | | | | | | | | | |
| t_{pd} | propagation delay | CP to Q; see Figure 5 | [2] | | | | | | |
| | | V_{CC} = 0.8 V | - | 23.6 | - | - | - | - | ns |
| | | V_{CC} = 1.1 V to 1.3 V | 2.4 | 6.3 | 13.1 | 2.3 | 13.3 | 2.3 | 13.4 |
| | | V_{CC} = 1.4 V to 1.6 V | 2.1 | 4.3 | 7.4 | 1.8 | 8.0 | 1.8 | 8.2 |
| | | V_{CC} = 1.65 V to 1.95 V | 1.6 | 3.4 | 5.8 | 1.4 | 6.4 | 1.4 | 6.7 |
| | | V_{CC} = 2.3 V to 2.7 V | 1.4 | 2.5 | 3.8 | 1.1 | 4.3 | 1.1 | 4.5 |
| t_{en} | enable time | OE to Q; see Figure 6 | [3] | | | | | | |
| | | V_{CC} = 0.8 V | - | 21.7 | - | - | - | - | ns |
| | | V_{CC} = 1.1 V to 1.3 V | 3.3 | 5.2 | 8.1 | 3.0 | 9.1 | 3.0 | 10.0 |
| | | V_{CC} = 1.4 V to 1.6 V | 2.6 | 4.1 | 5.6 | 2.4 | 6.1 | 2.4 | 6.7 |
| | | V_{CC} = 1.65 V to 1.95 V | 2.3 | 3.4 | 4.6 | 2.0 | 5.1 | 2.0 | 5.6 |
| | | V_{CC} = 2.3 V to 2.7 V | 2.0 | 2.8 | 3.7 | 1.8 | 4.0 | 1.8 | 4.4 |
| t_{dis} | disable time | OE to Q; see Figure 6 | [4] | | | | | | |
| | | V_{CC} = 0.8 V | - | 9.8 | - | - | - | - | ns |
| | | V_{CC} = 1.1 V to 1.3 V | 2.9 | 4.5 | 7.0 | 2.8 | 7.2 | 2.8 | 7.9 |
| | | V_{CC} = 1.4 V to 1.6 V | 2.3 | 3.3 | 4.9 | 2.1 | 5.1 | 2.1 | 5.6 |
| | | V_{CC} = 1.65 V to 1.95 V | 2.2 | 3.2 | 4.5 | 2.1 | 4.7 | 2.1 | 5.2 |
| | | V_{CC} = 2.3 V to 2.7 V | 1.6 | 2.3 | 3.1 | 1.5 | 3.4 | 1.5 | 3.7 |
| | | V_{CC} = 3.0 V to 3.6 V | 1.9 | 2.6 | 3.4 | 1.8 | 3.6 | 1.8 | 4.0 |

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

| Symbol | Parameter | Conditions | 25 °C | | | −40 °C to +125 °C | | | Unit |
|------------------------------|-------------------|---------------------------------------|---------------------|--------------------|------|-------------------|-------------|--------------|------|
| | | | Min | Typ ^[1] | Max | Min (85 °C) | Max (85 °C) | Min (125 °C) | |
| f _{max} | maximum frequency | CP; see Figure 5 | | | | | | | |
| | | V _{CC} = 0.8 V | - | 53 | - | - | - | - | MHz |
| | | V _{CC} = 1.1 V to 1.3 V | - | 203 | - | 170 | - | 170 | MHz |
| | | V _{CC} = 1.4 V to 1.6 V | - | 347 | - | 310 | - | 300 | MHz |
| | | V _{CC} = 1.65 V to 1.95 V | - | 435 | - | 400 | - | 390 | MHz |
| | | V _{CC} = 2.3 V to 2.7 V | - | 550 | - | 490 | - | 480 | MHz |
| | | V _{CC} = 3.0 V to 3.6 V | - | 619 | - | 550 | - | 510 | MHz |
| C_L = 10 pF | | | | | | | | | |
| t _{pd} | propagation delay | CP to Q; see Figure 5 | [2] | | | | | | |
| | | V _{CC} = 0.8 V | - | 27.1 | - | - | - | - | ns |
| | | V _{CC} = 1.1 V to 1.3 V | 2.7 | 7.2 | 14.7 | 2.5 | 15.0 | 2.5 | 15.1 |
| | | V _{CC} = 1.4 V to 1.6 V | 2.3 | 4.9 | 8.6 | 2.0 | 9.1 | 2.0 | 9.4 |
| | | V _{CC} = 1.65 V to 1.95 V | 2.1 | 4.0 | 6.5 | 1.9 | 7.0 | 1.9 | 7.3 |
| | | V _{CC} = 2.3 V to 2.7 V | 1.8 | 3.1 | 4.4 | 1.5 | 4.9 | 1.5 | 5.1 |
| | | V _{CC} = 3.0 V to 3.6 V | 1.6 | 2.7 | 3.7 | 1.3 | 4.0 | 1.3 | 4.2 |
| t _{en} | enable time | OE to Q; see Figure 6 | [3] | | | | | | |
| | | V _{CC} = 0.8 V | - | 25.1 | - | - | - | - | ns |
| | | V _{CC} = 1.1 V to 1.3 V | 3.8 | 6.5 | 10.2 | 3.5 | 10.6 | 3.5 | 11.7 |
| | | V _{CC} = 1.4 V to 1.6 V | 3.1 | 4.7 | 6.5 | 2.7 | 7.1 | 2.7 | 7.8 |
| | | V _{CC} = 1.65 V to 1.95 V | 2.7 | 4.0 | 5.4 | 2.5 | 6.0 | 2.5 | 6.6 |
| | | V _{CC} = 2.3 V to 2.7 V | 2.4 | 3.4 | 4.5 | 2.2 | 4.7 | 2.2 | 5.2 |
| | | V _{CC} = 3.0 V to 3.6 V | 2.3 | 3.1 | 4.1 | 2.1 | 4.2 | 2.1 | 4.6 |
| t _{dis} | disable time | OE to Q; see Figure 6 | [4] | | | | | | |
| | | V _{CC} = 0.8 V | - | 11.7 | - | - | - | - | ns |
| | | V _{CC} = 1.1 V to 1.3 V | 3.9 | 5.6 | 8.3 | 3.9 | 8.4 | 3.9 | 9.2 |
| | | V _{CC} = 1.4 V to 1.6 V | 3.1 | 4.2 | 5.8 | 3.0 | 6.1 | 3.0 | 6.7 |
| | | V _{CC} = 1.65 V to 1.95 V | 3.2 | 4.3 | 5.7 | 3.1 | 5.9 | 3.1 | 6.5 |
| | | V _{CC} = 2.3 V to 2.7 V | 2.3 | 3.1 | 4.0 | 2.2 | 4.2 | 2.2 | 4.6 |
| | | V _{CC} = 3.0 V to 3.6 V | 3.0 | 3.8 | 4.8 | 2.9 | 5.0 | 2.9 | 5.5 |
| f _{max} | maximum frequency | CP; see Figure 5 | | | | | | | |
| | | V _{CC} = 0.8 V | - | 52 | - | - | - | - | MHz |
| | | V _{CC} = 1.1 V to 1.3 V | - | 192 | - | 150 | - | 150 | MHz |
| | | V _{CC} = 1.4 V to 1.6 V | - | 324 | - | 280 | - | 230 | MHz |
| | | V _{CC} = 1.65 V to 1.95 V | - | 421 | - | 310 | - | 250 | MHz |
| | | V _{CC} = 2.3 V to 2.7 V | - | 486 | - | 370 | - | 360 | MHz |
| | | V _{CC} = 3.0 V to 3.6 V | - | 550 | - | 410 | - | 360 | MHz |

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

| Symbol | Parameter | Conditions | 25 °C | | | −40 °C to +125 °C | | | Unit | |
|---|-------------------|--|----------------|--------------------|------|-------------------|-------------|--------------|------|--|
| | | | Min | Typ ^[1] | Max | Min (85 °C) | Max (85 °C) | Min (125 °C) | | |
| $C_L = 15 \text{ pF}$ | | | | | | | | | | |
| t_{pd} | propagation delay | CP to Q; see Figure 5 | ^[2] | | | | | | | |
| | | $V_{CC} = 0.8 \text{ V}$ | - | 30.6 | - | - | - | - | ns | |
| | | $V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$ | 3.0 | 8.0 | 16.2 | 2.8 | 16.5 | 2.8 | 16.6 | |
| | | $V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$ | 2.8 | 5.5 | 9.3 | 2.4 | 10.1 | 2.4 | 10.4 | |
| | | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$ | 2.3 | 4.5 | 7.2 | 2.1 | 7.9 | 2.1 | 8.2 | |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 2.1 | 3.5 | 5.0 | 1.9 | 5.5 | 1.9 | 5.7 | |
| t_{en} | enable time | \overline{OE} to Q; see Figure 6 | ^[3] | | | | | | | |
| | | $V_{CC} = 0.8 \text{ V}$ | - | 28.6 | - | - | - | - | ns | |
| | | $V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$ | 4.3 | 7.4 | 11.6 | 3.9 | 12.1 | 3.9 | 13.3 | |
| | | $V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$ | 3.5 | 5.3 | 7.2 | 3.1 | 8.0 | 3.1 | 8.8 | |
| | | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$ | 3.1 | 4.5 | 6.1 | 2.8 | 6.7 | 2.8 | 7.4 | |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 2.7 | 3.8 | 5.0 | 2.5 | 5.4 | 2.5 | 5.9 | |
| t_{dis} | disable time | \overline{OE} to Q; see Figure 6 | ^[4] | | | | | | | |
| | | $V_{CC} = 0.8 \text{ V}$ | - | 13.5 | - | - | - | - | ns | |
| | | $V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$ | 5.0 | 6.8 | 9.5 | 4.9 | 9.6 | 4.9 | 10.6 | |
| | | $V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$ | 3.9 | 5.1 | 6.8 | 3.8 | 7.0 | 3.8 | 7.7 | |
| | | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$ | 4.3 | 5.4 | 7.0 | 4.1 | 7.2 | 4.1 | 7.9 | |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 3.0 | 3.9 | 4.9 | 2.9 | 5.1 | 2.9 | 5.6 | |
| f_{max} | maximum frequency | CP; see Figure 5 | ^[5] | | | | | | | |
| | | $V_{CC} = 0.8 \text{ V}$ | - | 50 | - | - | - | - | MHz | |
| | | $V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$ | - | 181 | - | 120 | - | 120 | - | |
| | | $V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$ | - | 301 | - | 190 | - | 160 | - | |
| | | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$ | - | 407 | - | 240 | - | 190 | - | |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | - | 422 | - | 300 | - | 270 | - | |
| $C_L = 30 \text{ pF}$ | | | | | | | | | | |
| t_{pd} | propagation delay | CP to Q; see Figure 5 | ^[2] | | | | | | | |
| | | $V_{CC} = 0.8 \text{ V}$ | - | 40.8 | - | - | - | - | ns | |
| | | $V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$ | 3.7 | 10.3 | 20.5 | 3.5 | 21.2 | 3.5 | 21.6 | |
| | | $V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$ | 3.3 | 7.0 | 11.6 | 3.2 | 12.6 | 3.2 | 13.3 | |
| | | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$ | 3.2 | 5.8 | 9.1 | 2.9 | 9.8 | 2.9 | 10.4 | |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 3.0 | 4.7 | 6.5 | 2.6 | 7.0 | 2.6 | 7.4 | |
| $C_L = 60 \text{ pF}$ | | | | | | | | | | |
| t_{pd} | propagation delay | CP to Q; see Figure 5 | ^[2] | | | | | | | |
| | | $V_{CC} = 0.8 \text{ V}$ | - | 40.8 | - | - | - | - | ns | |
| | | $V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$ | 3.7 | 10.3 | 20.5 | 3.5 | 21.2 | 3.5 | 21.6 | |
| | | $V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$ | 3.3 | 7.0 | 11.6 | 3.2 | 12.6 | 3.2 | 13.3 | |
| | | $V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$ | 3.2 | 5.8 | 9.1 | 2.9 | 9.8 | 2.9 | 10.4 | |
| | | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ | 3.0 | 4.7 | 6.5 | 2.6 | 7.0 | 2.6 | 7.4 | |

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

| Symbol | Parameter | Conditions | 25 °C | | | −40 °C to +125 °C | | | Unit |
|---|-------------------|---|-------|--------------------|-----|-------------------|-------------|--------------|------|
| | | | Min | Typ ^[1] | Max | Min (85 °C) | Max (85 °C) | Min (125 °C) | |
| t _{en} | enable time | OE to Q; see Figure 6 | [3] | [3] | | | | | ns |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| t _{dis} | disable time | OE to Q; see Figure 6 | [4] | [4] | | | | | ns |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| f _{max} | maximum frequency | CP; see Figure 5 | | | | | | | MHz |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| C_L = 5 pF, 10 pF, 15 pF and 30 pF | | | | | | | | | |
| t _w | pulse width | CP; HIGH or LOW; see Figure 5 | | | | | | | ns |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| t _{su(H)} | set-up time HIGH | D to CP; see Figure 5 | | | | | | | ns |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

| Symbol | Parameter | Conditions | 25 °C | | | −40 °C to +125 °C | | | Unit |
|-------------|-------------------------------------|--|-----------------------------|--------------------|------|-------------------|-------------|--------------|------|
| | | | Min | Typ ^[1] | Max | Min (85 °C) | Max (85 °C) | Min (125 °C) | |
| $t_{su(L)}$ | set-up time LOW | D to CP; see Figure 5 | $V_{CC} = 0.8$ V | - | 3.5 | - | - | - | ns |
| | | | $V_{CC} = 1.1$ V to 1.3 V | - | 0.8 | - | 1.8 | - | ns |
| | | | $V_{CC} = 1.4$ V to 1.6 V | - | 0.6 | - | 1.2 | - | ns |
| | | | $V_{CC} = 1.65$ V to 1.95 V | - | 0.5 | - | 1.1 | - | ns |
| | | | $V_{CC} = 2.3$ V to 2.7 V | - | 0.4 | - | 1.0 | - | ns |
| | | | $V_{CC} = 3.0$ V to 3.6 V | - | 0.5 | - | 1.0 | - | ns |
| t_h | hold time | D to CP; see Figure 5 | $V_{CC} = 0.8$ V | - | −2.8 | - | - | - | ns |
| | | | $V_{CC} = 1.1$ V to 1.3 V | - | −0.7 | - | 0 | - | ns |
| | | | $V_{CC} = 1.4$ V to 1.6 V | - | −0.4 | - | 0 | - | ns |
| | | | $V_{CC} = 1.65$ V to 1.95 V | - | −0.4 | - | 0 | - | ns |
| | | | $V_{CC} = 2.3$ V to 2.7 V | - | −0.3 | - | 0 | - | ns |
| | | | $V_{CC} = 3.0$ V to 3.6 V | - | −0.4 | - | 0 | - | ns |
| C_{PD} | power dissipation capacitance | $V_I = \text{GND to } V_{CC}$; [5] $f_i = 1$ MHz; output enabled | $V_{CC} = 0.8$ V | - | 1.7 | - | - | - | pF |
| | | | $V_{CC} = 1.1$ V to 1.3 V | - | 1.8 | - | - | - | pF |
| | | | $V_{CC} = 1.4$ V to 1.6 V | - | 1.8 | - | - | - | pF |
| | | | $V_{CC} = 1.65$ V to 1.95 V | - | 2.0 | - | - | - | pF |
| | | | $V_{CC} = 2.3$ V to 2.7 V | - | 2.3 | - | - | - | pF |
| | | | $V_{CC} = 3.0$ V to 3.6 V | - | 2.8 | - | - | - | pF |

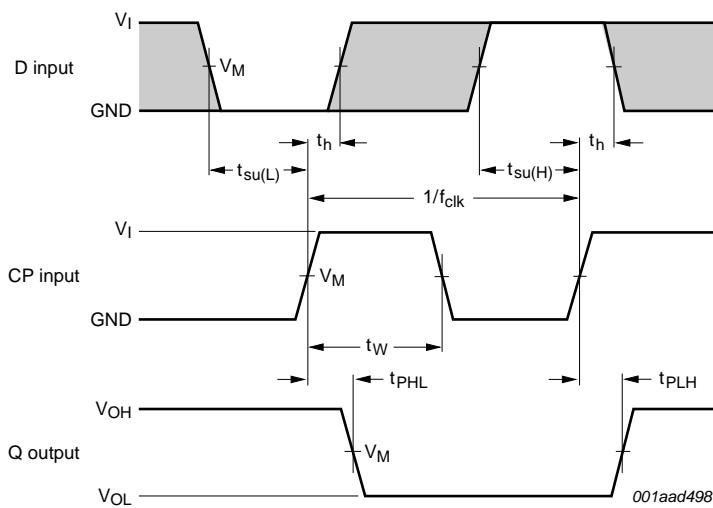
[1] All typical values are measured at nominal V_{CC} .[2] t_{pd} is the same as t_{PLH} and t_{PHL} .[3] t_{en} is the same as t_{PZH} and t_{PZL} .[4] t_{dis} is the same as t_{PHZ} and t_{PLZ} .[5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

 f_i = input frequency in MHz; f_o = output frequency in MHz; C_L = output load capacitance in pF; V_{CC} = supply voltage in V; $\sum(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs;

N = number of inputs switching.

12. Waveforms



Measurement points are given in [Table 9](#).

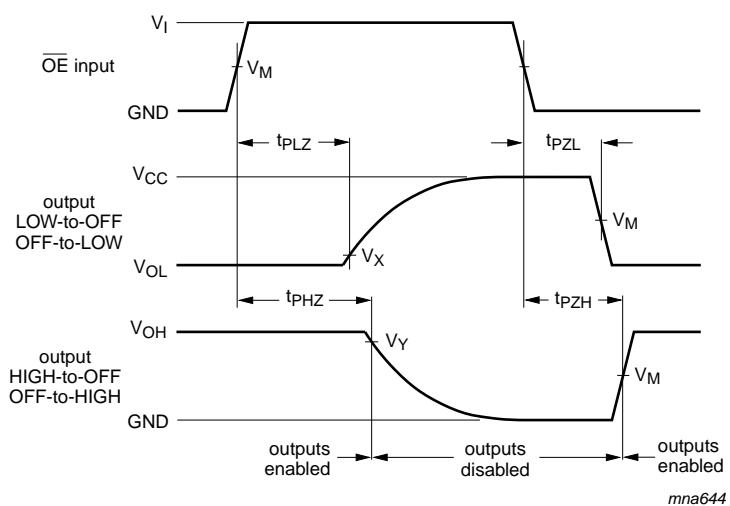
The shaded areas indicate when the input is permitted to change for predictable output performance.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig 5. Clock propagation delays, pulse width, set-up times and maximum frequency (CP)

Table 9. Measurement points

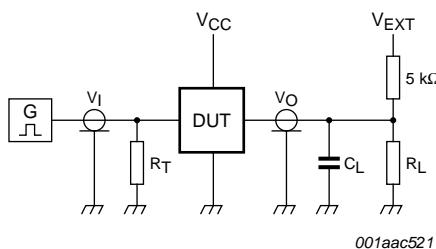
| Supply voltage | Input | | | | Output | | |
|-----------------|---------------------|----------|---------------|---------------------|-------------------|-------------------|--|
| | V_M | V_I | $t_r = t_f$ | V_M | V_X | V_Y | |
| V_{CC} | | | | | | | |
| 0.8 V to 1.6 V | $0.5 \times V_{CC}$ | V_{CC} | ≤ 3.0 ns | $0.5 \times V_{CC}$ | $V_{OL} + 0.1$ V | $V_{OH} - 0.1$ V | |
| 1.65 V to 2.7 V | $0.5 \times V_{CC}$ | V_{CC} | ≤ 3.0 ns | $0.5 \times V_{CC}$ | $V_{OL} + 0.15$ V | $V_{OH} - 0.15$ V | |
| 3.0 V to 3.6 V | $0.5 \times V_{CC}$ | V_{CC} | ≤ 3.0 ns | $0.5 \times V_{CC}$ | $V_{OL} + 0.3$ V | $V_{OH} - 0.3$ V | |



Measurement points are given in [Table 9](#).

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig 6. Enable and disable times



Test data is given in [Table 10](#).

Definitions for test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

V_{EXT} = External voltage for measuring switching times.

Fig 7. Test circuit for measuring switching times

Table 10. Test data

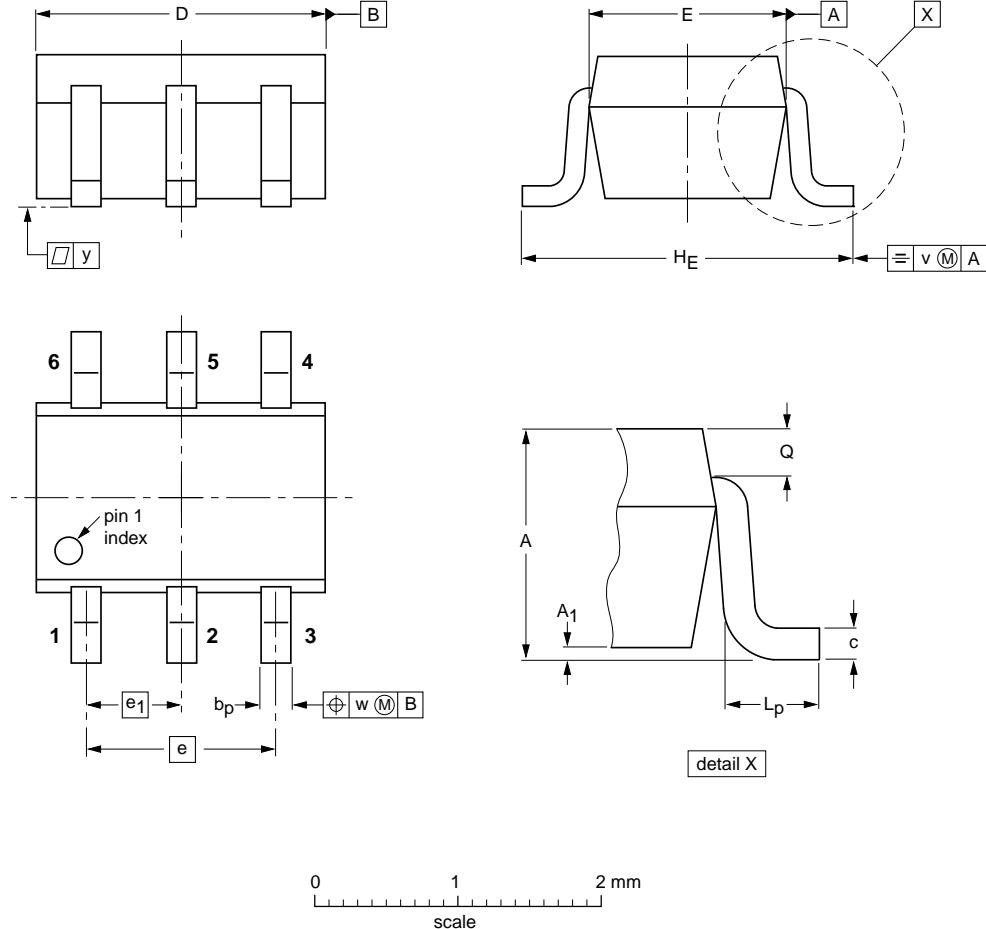
| Supply voltage | Load | | V_{EXT} |
|----------------|------------------------------|----------------------|--------------------|
| V_{CC} | C_L | R_L ^[1] | t_{PLH}, t_{PHL} |
| 0.8 V to 3.6 V | 5 pF, 10 pF, 15 pF and 30 pF | 5 kΩ or 1 MΩ | open |

[1] For measuring enable and disable times, $R_L = 5 \text{ k}\Omega$. For measuring propagation delays, set-up and hold times and pulse width, $R_L = 1 \text{ M}\Omega$.

13. Package outline

Plastic surface-mounted package; 6 leads

SOT363



DIMENSIONS (mm are the original dimensions)

| UNIT | A | A_1 max | b_p | c | D | E | e | e_1 | H_E | L_p | Q | v | w | y |
|------|------------|--------------|--------------|--------------|------------|--------------|-----|-------|------------|--------------|--------------|-----|-----|-----|
| mm | 1.1 0.8 | 0.1 | 0.30 0.20 | 0.25 0.10 | 2.2 1.8 | 1.35 1.15 | 1.3 | 0.65 | 2.2 2.0 | 0.45 0.15 | 0.25 0.15 | 0.2 | 0.2 | 0.1 |

| OUTLINE VERSION | REFERENCES | | | | EUROPEAN PROJECTION | ISSUE DATE |
|--------------------|------------|-------|-------|--|------------------------|----------------------|
| | IEC | JEDEC | JEITA | | | |
| SOT363 | | | SC-88 | | | 04-11-08 06-03-16 |

Fig 8. Package outline SOT363 (SC-88)

14. Abbreviations

Table 11. Abbreviations

| Acronym | Description |
|---------|-------------------------|
| CDM | Charged Device Model |
| DUT | Device Under Test |
| ESD | ElectroStatic Discharge |
| HBM | Human Body Model |
| MM | Machine Model |
| MIL | Military |

15. Revision history

Table 12. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|---------------------|--------------|--------------------|---------------|------------|
| 74AUP1G374_Q100 v.1 | 20130219 | Product data sheet | - | - |

16. Legal information

16.1 Data sheet status

| Document status ^{[1][2]} | Product status ^[3] | Definition |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

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